RELIABILITY REPORT
FOR
MAX4372TEUK+
PLASTIC ENCAPSULATED DEVICES

February 25, 2009

MAXIM INTEGRATED PRODUCTS
120 SAN GABRIEL DR.
SUNNYVALE, CA 94086

Approved by
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Quality Assurance
Director, Reliability Engineering
Conclusion

The MAX4372TEUK+ successfully meets the quality and reliability standards required of all Maxim products. In addition, Maxim's continuous reliability monitoring program ensures that all outgoing product will continue to meet Maxim's quality and reliability standards.

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I. Device Description

A. General

The MAX4372 low-cost, precision, high-side current-sense amplifier is available in a tiny, space-saving SOT23-5-pin package. Offered in three gain versions (T = 20V/V, F = 50V/V, and H = 100V/V), this device operates from a single 2.7V to 28V supply and consumes only 30µA. It features a voltage output that eliminates the need for gain-setting resistors and is ideal for today's notebook computers, cell phones, and other systems where battery/DC current monitoring is critical. High-side current monitoring is especially useful in battery-powered systems since it does not interfere with the ground path of the battery charger. The input common-mode range of 0 to 28V is independent of the supply voltage and ensures that the current-sense feedback remains viable even when connected to a 2-cell battery pack in deep discharge. The user can set the full-scale current reading by choosing the device (T, F, or H) with the desired voltage gain and selecting the appropriate external sense resistor. This capability offers a high level of integration and flexibility, resulting in a simple and compact current-sense solution.
II. Manufacturing Information

A. Description/Function: Low-Cost, UCSP/SOT23, Micropower, High-Side Current-Sense Amplifier with Voltage Output

B. Process: B12
C. Number of Device Transistors: 225
D. Fabrication Location: Oregon, California
E. Assembly Location: Malaysia, Thailand
F. Date of Initial Production: October 21, 1999

III. Packaging Information

A. Package Type: 5-pin SOT23
B. Lead Frame: Copper
C. Lead Finish: 100% matte Tin
D. Die Attach: Non-conductive Epoxy
E. Bondwire: Gold (1 mil dia.)
F. Mold Material: Epoxy with silica filler
G. Assembly Diagram: #05-9000-2243
H. Flammability Rating: Class UL94-V0
I. Classification of Moisture Sensitivity per JEDEC standard J-STD-020-C: Level 1
J. Single Layer Theta Ja: 324.3°C/W
K. Single Layer Theta Jc: 82°C/W
L. Multi Layer Theta Ja: 255.9°C/W
M. Multi Layer Theta Jc: 81°C/W

IV. Die Information

A. Dimensions: 63 X 42 mils
B. Passivation: Si₃N₄/SiO₂ (Silicon nitride/ Silicon dioxide)
C. Interconnect: Aluminum/Si (Si = 1%)
D. Backside Metallization: None
E. Minimum Metal Width: 1.2 microns (as drawn)
F. Minimum Metal Spacing: 1.2 microns (as drawn)
G. Bondpad Dimensions: 5 mil. Sq.
H. Isolation Dielectric: SiO₂
I. Die Separation Method: Wafer Saw
V. Quality Assurance Information

A. Quality Assurance Contacts: Ken Wendel (Director, Reliability Engineering)
   Bryan Preeshl (Managing Director of QA)

B. Outgoing Inspection Level: 0.1% for all electrical parameters guaranteed by the Datasheet.
   0.1% For all Visual Defects.

C. Observed Outgoing Defect Rate: < 50 ppm

D. Sampling Plan: Mil-Std-105D

VI. Reliability Evaluation

A. Accelerated Life Test

   The results of the 135°C biased (static) life test are shown in Table 1. Using these results, the Failure Rate (\( \lambda \)) is calculated as follows:

   \[
   \frac{1}{\text{MTTF}} = \frac{1.83}{192 \times 4340 \times 159 \times 2} \quad \text{(Chi square value for MTTF upper limit)}
   \]

   \( \lambda = 6.83 \times 10^{-9} \)

   \( \lambda = 6.83 \text{ F.I.T. (60% confidence level @ 25°C)} \)

   The following failure rate represents data collected from Maxim’s reliability monitor program. Maxim performs quarterly 1000 hour life test monitors on its processes. This data is published in the Product Reliability Report found at http://www.maxim-ic.com/. Current monitor data for the B12 Process results in a FIT Rate of 3.13 @ 25C and 54.16 @ 55C (0.8 eV, 60% UCL)

B. Moisture Resistance Tests

   The industry standard 85°C/85%RH or HAST testing is monitored per device process once a quarter.

C. E.S.D. and Latch-Up Testing

   The OP85 die type has been found to have all pins able to withstand a HBM transient pulse of +/-2000V per Mil-Std 883 Method 3015.7. Latch-Up testing has shown that this device withstands a current of +/-250mA.
### Table 1
Reliability Evaluation Test Results

<table>
<thead>
<tr>
<th>TEST ITEM</th>
<th>TEST CONDITION</th>
<th>FAILURE IDENTIFICATION</th>
<th>SAMPLE SIZE</th>
<th>NUMBER OF FAILURES</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Static Life Test</strong> (Note 1)</td>
<td>Ta = 135°C Biased Time = 192 hrs.</td>
<td>DC Parameters &amp; functionality</td>
<td>159</td>
<td>0</td>
</tr>
<tr>
<td><strong>Moisture Testing</strong> (Note 2)</td>
<td>Ta = 85°C RH = 85% Biased Time = 1000hrs.</td>
<td>DC Parameters &amp; functionality</td>
<td>77</td>
<td>0</td>
</tr>
<tr>
<td><strong>Mechanical Stress</strong> (Note 2)</td>
<td>Temperature -65°C/150°C Cycle 1000 Cycles Method 1010</td>
<td>DC Parameters &amp; functionality</td>
<td>77</td>
<td>0</td>
</tr>
</tbody>
</table>

Note 1: Life Test Data may represent plastic DIP qualification lots.
Note 2: Generic Package/Process data