RELIABILITY REPORT
FOR
MAX3243xxI
PLASTIC ENCAPSULATED DEVICES

July 5, 2005

MAXIM INTEGRATED PRODUCTS
120 SAN GABRIEL DR.
SUNNYVALE, CA 94086

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Quality Assurance
Managing Director
Conclusion

The MAX3243 successfully meets the quality and reliability standards required of all Maxim products. In addition, Maxim’s continuous reliability monitoring program ensures that all outgoing product will continue to meet Maxim’s quality and reliability standards.

Table of Contents

I.  Device Description
II.  Manufacturing Information
III.  Packaging Information
IV.  Die Information
V.  Quality Assurance Information
VI.  Reliability Evaluation
......Attachments

I. Device Description

A. General

The MAX3243 achieve 1µA supply current with Maxim’s revolutionary AutoShutdown™ feature. When the MAX3243 do not sense a valid signal level on their receiver inputs, the on-board power supply and drivers shut down. This occurs if the RS-232 cable is disconnected or if the transmitters of the connected peripheral are turned off. The system turns on again when a valid level is applied to any RS-232 receiver input. As a result, the system saves power without changes to the existing BIOS or operating system.

The MAX3243 transceiver is a 3V-powered EIA/TIA-232 and V.28/V.24 communications interface intended for notebook computer applications. A proprietary, high-efficiency, dual charge-pump power supply and a low-dropout transmitter combine to deliver true RS-232 performance from a single +3.0V to +5.5V supply. A guaranteed data rate of 120kbps provides compatibility with popular software for communicating with personal computers.

The MAX3243 require only 0.1µF capacitors in 3.3V operation, and can operate from input voltages ranging from +3.0V to +5.5V. It is ideal for 3.3V-only systems, mixed 3.3V and 5.0V systems, or 5.0V-only systems that require true RS-232 performance.

The MAX3243 3-driver/5-receiver, complete serial port is ideal for notebook or subnotebook computers. The MAX3243 includes one complementary always-active receiver. This receiver can monitor an external device (such as a modem) in shutdown, without forward biasing the protection diodes in a UART that may have VCC completely removed.

B. Absolute Maximum Ratings

<table>
<thead>
<tr>
<th>Item</th>
<th>Rating</th>
</tr>
</thead>
<tbody>
<tr>
<td>VCC</td>
<td>0.3V to +6V</td>
</tr>
<tr>
<td>V+ (Note 1)</td>
<td>0.3V to +7V</td>
</tr>
<tr>
<td>V- (Note 1)</td>
<td>0.3V to -7V</td>
</tr>
<tr>
<td>V+ +</td>
<td>13V</td>
</tr>
<tr>
<td></td>
<td>Note 1: V+ and V- can have maximum magnitudes of 7V, but their absolute difference cannot exceed 13V.</td>
</tr>
</tbody>
</table>
II. Manufacturing Information

A. Description/Function: 1µA Supply-Current, True +3V to +5.5V RS-232 Transceiver with AutoShutdown

B. Process: S3 (Standard 3 micron silicon gate CMOS)

C. Number of Device Transistors: 476

D. Fabrication Location: Oregon, USA

E. Assembly Location: Philippines or Malaysia

F. Date of Initial Production: March, 1995

III. Packaging Information

A. Package Type: 28-Lead WSO  28-Lead SSOP  28-Lead TSSOP

B. Lead Frame: Copper  Copper  Copper

C. Lead Finish: Solder Plate or 100% Matte Tin (all packages)

D. Die Attach: Silver-filled Epoxy  Silver-filled Epoxy  Silver-filled Epoxy

E. Bondwire: Gold (1.3 mil dia.)  Gold (1.3 mil dia.)  Gold (1.3 mil dia.)

F. Mold Material: Epoxy with silica filler  Epoxy with silica filler  Epoxy with silica filler

G. Assembly Diagram: # 05-1901-0052  #05-1901-0053  #05-1901-0206

H. Flammability Rating: Class UL94-V0  Class UL94-V0  Class UL94-V0

I. Classification of Moisture Sensitivity per JEDEC standard J-STD-020-C: Level 1  Level 1  Level 1

IV. Die Information

A. Dimensions: 91 x 151 mils

B. Passivation: Si$_3$N$_4$/SiO$_2$ (Silicon nitride/ Silicon dioxide)

C. Interconnect: Aluminum/Si (Si = 1%)

D. Backside Metallization: None

E. Minimum Metal Width: 3 microns (as drawn)

F. Minimum Metal Spacing: 3 microns (as drawn)

G. Bondpad Dimensions: 5 mil. Sq.

H. Isolation Dielectric: SiO$_2$

I. Die Separation Method: Wafer Saw
V. Quality Assurance Information

A. Quality Assurance Contacts: Jim Pedicord (Manager, Reliability Operations) Bryan Preeshl (Managing Director of QA)

B. Outgoing Inspection Level: 0.1% for all electrical parameters guaranteed by the Datasheet. 0.1% For all Visual Defects.

C. Observed Outgoing Defect Rate: < 50 ppm

D. Sampling Plan: Mil-Std-105D

VI. Reliability Evaluation

A. Accelerated Life Test

The results of the 135°C biased (static) life test are shown in Table 1. Using these results, the Failure Rate (λ) is calculated as follows:

\[ \lambda = \frac{1}{MTTF} = \frac{1.83}{192 \times 4389 \times 960 \times 2} \]

\[ \text{Temperature Acceleration factor assuming an activation energy of } 0.8eV \]

\[ \lambda = 1.13 \times 10^{-9} \quad \lambda = 1.13 \text{ F.I.T. (60% confidence level @ 25°C)} \]

This low failure rate represents data collected from Maxim’s reliability qualification and monitor programs. Maxim also performs weekly Burn-In on samples from production to assure reliability of its processes. The reliability required for lots which receive a burn-in qualification is 59 F.I.T. at a 60% confidence level, which equates to 3 failures in an 80 piece sample. Maxim performs failure analysis on rejects from lots exceeding this level. The attached Burn-In Schematic (Spec. # 06-5167) shows the static circuit used for this test. Maxim also performs 1000 hour life test monitors quarterly for each process. This data is published in the Product Reliability Report (RR-1N).

B. Moisture Resistance Tests

Maxim evaluates pressure pot stress from every assembly process during qualification of each new design. Pressure Pot testing must pass a 20% LTPD for acceptance. Additionally, industry standard 85°C/85%RH or HAST tests are performed quarterly per device/package family.

C. E.S.D. and Latch-Up Testing

The RS16-1 die type has been found to have all pins able to withstand a transient pulse of ±2500V, per Mil-Std-883 Method 3015 (reference attached ESD Test Circuit). Latch-Up testing has shown that this device withstands a current of ±250mA.
Table 1
Reliability Evaluation Test Results

MAX3243xxI

<table>
<thead>
<tr>
<th>TEST ITEM</th>
<th>TEST CONDITION</th>
<th>FAILURE IDENTIFICATION</th>
<th>PACKAGE</th>
<th>SAMPLE SIZE</th>
<th>NUMBER OF FAILURES</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Static Life Test</strong> (Note 1)</td>
<td>Ta = 135°C Biased Time = 192 hrs.</td>
<td>DC Parameters &amp; functionality</td>
<td>960</td>
<td></td>
<td>0</td>
</tr>
<tr>
<td><strong>Moisture Testing</strong> (Note 2)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Pressure Pot</td>
<td>Ta = 121°C P = 15 psi. RH= 100% Time = 168hrs.</td>
<td>DC Parameters &amp; functionality</td>
<td>77</td>
<td></td>
<td>0</td>
</tr>
<tr>
<td>85/85</td>
<td>Ta = 85°C RH = 85% Biased Time = 1000hrs.</td>
<td>DC Parameters &amp; functionality</td>
<td>77</td>
<td></td>
<td>0</td>
</tr>
<tr>
<td><strong>Mechanical Stress</strong> (Note 2)</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Temperature Cycle</td>
<td>-65°C/150°C 1000 Cycles Method 1010</td>
<td>DC Parameters &amp; functionality</td>
<td>77</td>
<td></td>
<td>0</td>
</tr>
</tbody>
</table>

Note 1: Life Test Data may represent plastic DIP qualification lots.
Note 2: Generic package/process data.
TABLE II. Pin combination to be tested. 1/ 2/

<table>
<thead>
<tr>
<th>Terminal A</th>
<th>Terminal B</th>
</tr>
</thead>
<tbody>
<tr>
<td>(Each pin individually</td>
<td>(The common combination</td>
</tr>
<tr>
<td>connected to terminal A</td>
<td>of all like-named pins</td>
</tr>
<tr>
<td>with the other floating)</td>
<td>connected to terminal B)</td>
</tr>
<tr>
<td>1.  All pins except $V_{PS1}$ 3/</td>
<td>All $V_{PS1}$ pins</td>
</tr>
<tr>
<td>2. All input and output</td>
<td>All other input-output pins</td>
</tr>
<tr>
<td>pins</td>
<td></td>
</tr>
</tbody>
</table>

1/ Table II is restated in narrative form in 3.4 below.
2/ No connects are not to be tested.
3/ Repeat pin combination I for each named Power supply and for ground
(e.g., where $V_{PS1}$ is $V_{DD}$, $V_{CC}$, $V_{SS}$, $V_{BB}$, GND, $+V_S$, $-V_S$, $V_{REF}$, etc).

3.4 Pin combinations to be tested.

a. Each pin individually connected to terminal A with respect to the device ground pin(s) connected to terminal B. All pins except the one being tested and the ground pin(s) shall be open.

b. Each pin individually connected to terminal A with respect to each different set of a combination of all named power supply pins (e.g., $V_{SS1}$, or $V_{SS2}$ or $V_{SS3}$ or $V_{CC1}$, or $V_{CC2}$) connected to terminal B. All pins except the one being tested and the power supply pin or set of pins shall be open.

c. Each input and each output individually connected to terminal A with respect to a combination of all the other input and output pins connected to terminal B. All pins except the input or output pin being tested and the combination of all the other input and output pins shall be open.
ONCE PER SOCKET

ONCE PER BOARD

- +5V
- 100 OHMS
- 0.68 uF
- 0.68 uF
- 0.68 uF
- 100 uF
- 0.1 uF

DEVICES: MAX3241/3243/3244
PACKAGE: 28-SSOP
MAX. EXPECTED CURRENT: 6mA