RELIABILITY REPORT
FOR
MAX30102EFD+T
PLASTIC ENCAPSULATED DEVICES

February 12, 2016

MAXIM INTEGRATED
160 RIO ROBLES
SAN JOSE, CA 95134

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<th>Approved by</th>
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<tbody>
<tr>
<td>Eric Wright</td>
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<tr>
<td>Quality Assurance</td>
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<tr>
<td>Reliability Engineer</td>
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</table>
Conclusion

The MAX30102EFD+T successfully meets the quality and reliability standards required of all Maxim Integrated products. In addition, Maxim Integrated's continuous reliability monitoring program ensures that all outgoing product will continue to meet Maxim Integrated's quality and reliability standards.

Table of Contents

I. Device Description
   A. General

The MAX30102 is an integrated pulse oximetry and heart-rate monitor module. It includes internal LEDs, photodetectors, optical elements, and low-noise electronics with ambient light rejection. The MAX30102 provides a complete system solution to ease the design-in process for mobile and wearable devices.

The MAX30102 operates on a single 1.8V power supply and a separate 5.0V power supply for the internal LEDs. Communication is through a standard I²C-compatible interface. The module can be shut down through software with zero standby current, allowing the power rails to remain powered at all times.

I. Device Description
   A. General
II. Manufacturing Information

A. Description/Function: High-Sensitivity Pulse Oximeter and Heart-Rate Sensor for Wearable Health
B. Process: S18
C. Number of Device Transistors: 176175
D. Fabrication Location: USA
E. Assembly Location: Taiwan
F. Date of Initial Production: October 30, 2015

III. Packaging Information

A. Package Type: 14-pin OE SiP
B. Lead Frame: N/A
C. Lead Finish: AuNi
D. Bondwire: Au (1 mil dia.)
E. Mold Material: Epoxy with silica filler
F. Assembly Diagram: #05-100244
G. Flammability Rating: Class UL94-V0
H. Classification of Moisture Sensitivity per JEDEC standard J-STD-020-C: Level 3
I. Single Layer Theta Ja: N/A°C/W
J. Single Layer Theta Jc: N/A°C/W
K. Multi Layer Theta Ja: 180°C/W
L. Multi Layer Theta Jc: 150°C/W

IV. Die Information

A. Dimensions: 62.9921X102.3622 mils
B. Passivation: Si₃N₄/SiO₂ (Silicon nitride/ Silicon dioxide)
C. Interconnect: Al/0.5%Cu with Ti/TiN Barrier
D. Backside Metallization: None
E. Minimum Metal Width: 0.23 microns (as drawn)
F. Minimum Metal Spacing: 0.23 microns (as drawn)
G. Bondpad Dimensions:
H. Isolation Dielectric: SiO₂
I. Die Separation Method: Wafer Saw
V. Quality Assurance Information

A. Quality Assurance Contacts:
   Eric Wright (Reliability Engineering)
   Bryan Preeshl (Vice President of QA)

B. Outgoing Inspection Level:
   0.1% for all electrical parameters guaranteed by the Datasheet.
   0.1% for all Visual Defects.

C. Observed Outgoing Defect Rate:
   < 50 ppm

D. Sampling Plan:
   Mil-Std-105D

VI. Reliability Evaluation

A. Accelerated Life Test

The results of the 135°C biased (static) life test are shown in Table 1. Using these results, the Failure Rate (λ) is calculated as follows:

\[
\frac{1}{\text{MTTF}} = 1.83 \quad \text{(Chi square value for MTTF upper limit)}
\]

(Ch square value for MTTF upper limit)

\[
\lambda \text{ (where 4340 = Temperature Acceleration factor assuming an activation energy of 0.8eV)}
\]

\[
\lambda = 1.83 \times 10^{-9}
\]

\[
\lambda = 1.83 \text{ F.I.T. (60% confidence level @ 25°C)}
\]

The following failure rate represents data collected from Maxim Integrated’s reliability monitor program. Maxim Integrated performs quarterly life test monitors on its processes. This data is published in the Reliability Report found at http://www.maximintegrated.com/qa/reliability/monitor. Cumulative monitor data for the S18 Process results in a FIT Rate of 0.40 @ 25°C and 6.96 @ 55°C (0.8 eV, 60% UCL)

B. E.S.D. and Latch-Up Testing

The OS24-0 die type has been found to have all pins able to withstand an HBM transient pulse of +/-2500V per JEDEC JESD22-A114. Latch-Up testing has shown that this device withstands a current of +/-250mA and overvoltage per JEDEC JESD78.
Table 1
Reliability Evaluation Test Results
MAX30102EFD+T

<table>
<thead>
<tr>
<th>TEST ITEM</th>
<th>TEST CONDITION</th>
<th>FAILURE IDENTIFICATION</th>
<th>SAMPLE SIZE</th>
<th>NUMBER OF FAILURES</th>
<th>COMMENTS</th>
</tr>
</thead>
<tbody>
<tr>
<td>Static Life Test</td>
<td>Ta = 135°C</td>
<td>DC Parameters &amp; functionality</td>
<td>231</td>
<td>0</td>
<td></td>
</tr>
<tr>
<td></td>
<td>Biased Time = 192 hrs.</td>
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Note 1: Life Test Data may represent plastic DIP qualification lots.