RELIABILITY REPORT
FOR
MAX2180ETG+
PLASTIC ENCAPSULATED DEVICES

March 3, 2011

MAXIM INTEGRATED PRODUCTS
120 SAN GABRIEL DR.
SUNNYVALE, CA 94086

Approved by
Sokhom Chum
Quality Assurance
Reliability Engineer
Conclusion

The MAX2180ETG+ successfully meets the quality and reliability standards required of all Maxim products. In addition, Maxim's continuous reliability monitoring program ensures that all outgoing product will continue to meet Maxim's quality and reliability standards.

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I. Device Description

A. General

The MAX2180 is a highly integrated AM/FM variable-gain low-noise amplifier ideal for use in automotive active antenna applications. The device features separate AM and FM signal paths, each providing 30dB of gain range, controlled by individual on-chip power detectors. The AM signal path covers a 148kHz to 30MHz input frequency range, while the FM signal path covers 65MHz to 162.5MHz. The device integrates a voltage regulator and pass transistor, allowing operation using battery voltages in the +8V to +24V range. On-chip thermal protection automatically limits junction temperatures during extreme thermal conditions. The device is available in a small, 4mm x 4mm, TQFN package and operates over the extended industrial temperature range (-40°C to +85°C).
II. Manufacturing Information

A. Description/Function: AM/FM Car Antenna Low-Noise Amplifier
B. Process: S18
C. Number of Device Transistors: 1283
D. Fabrication Location: California
E. Assembly Location: China
F. Date of Initial Production: June 24, 2010

III. Packaging Information

A. Package Type: 24-pin TQFN 4x4
B. Lead Frame: Copper
C. Lead Finish: 100% matte Tin
D. Die Attach: Conductive
E. Bondwire: Au (0.8 mil dia.)
F. Mold Material: Epoxy with silica filler
G. Assembly Diagram: #05-9000-4038
H. Flammability Rating: Class UL94-V0
I. Classification of Moisture Sensitivity per JEDEC standard J-STD-020-C Level 1
J. Single Layer Theta Ja: 48°C/W
K. Single Layer Theta Jc: 2.7°C/W
L. Multi Layer Theta Ja: 36°C/W
M. Multi Layer Theta Jc: 2.7°C/W

IV. Die Information

A. Dimensions: 54.33 X 58.27 mils
B. Passivation: Si$_3$N$_4$/SiO$_2$ (Silicon nitride/ Silicon dioxide)
C. Interconnect: Al with Ti/TiN Barrier
D. Backside Metallization: None
E. Minimum Metal Width: 0.18µm
F. Minimum Metal Spacing: 0.18µm
G. Bondpad Dimensions: 5 mil. Sq.
H. Isolation Dielectric: SiO$_2$
I. Die Separation Method: Wafer Saw
V. Quality Assurance Information

A. Quality Assurance Contacts: Richard Aburano (Manager, Reliability Engineering)
   Don Lipps (Manager, Reliability Engineering)
   Bryan Preeshl (Vice President of QA)

B. Outgoing Inspection Level: 0.1% for all electrical parameters guaranteed by the Datasheet.
   0.1% For all Visual Defects.

C. Observed Outgoing Defect Rate: < 50 ppm

D. Sampling Plan: Mil-Std-105D

VI. Reliability Evaluation

A. Accelerated Life Test

The results of the 135°C biased (static) life test are shown in Table 1. Using these results, the Failure Rate (λ) is calculated as follows:

\[
\lambda = \frac{1}{\text{MTTF}} = \frac{1.83}{192 \times 4340 \times 48 \times 2}
\]

(Chi square value for MTTF upper limit)

(where 4340 = Temperature Acceleration factor assuming an activation energy of 0.8eV)

\[
\lambda = 22.9 \times 10^{-9}
\]

\[
\lambda = 22.9 \text{ F.I.T.} \ (60\% \text{ confidence level @ 25°C})
\]

The following failure rate represents data collected from Maxim's reliability monitor program. Maxim performs quarterly life test monitors on its processes. This data is published in the Reliability Report found at http://www.maxim-ic.com/qa/reliability/monitor. Cumulative monitor data for the S18 Process results in a FIT Rate of 0.40 @ 25C and 6.96 @ 55C (0.8 eV, 60% UCL)

B. E.S.D. and Latch-Up Testing (lot SH7ZB3001B D/C 1020)

The WV30 die type has been found to have all pins able to withstand a HBM transient pulse of +/-2500V per JEDEC JESD22-A114. Latch-Up testing has shown that this device withstands a current of +/-100mA and overvoltage per JEDEC JESD78.
Table 1
Reliability Evaluation Test Results

MAX2180ETG+

<table>
<thead>
<tr>
<th>TEST ITEM</th>
<th>TEST CONDITION</th>
<th>FAILURE IDENTIFICATION</th>
<th>SAMPLE SIZE</th>
<th>NUMBER OF FAILURES</th>
<th>COMMENTS</th>
</tr>
</thead>
<tbody>
<tr>
<td>Static Life Test</td>
<td>Ta = 135°C</td>
<td>DC Parameters</td>
<td>48</td>
<td>0</td>
<td>SH7ZB3001B, D/C 1020</td>
</tr>
<tr>
<td></td>
<td>Biased</td>
<td>&amp; functionality</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>Time = 192 hrs.</td>
<td></td>
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<td></td>
<td></td>
</tr>
</tbody>
</table>

Note 1: Life Test Data may represent plastic DIP qualification lots.