



10/8/10

**RELIABILITY MONITOR REPORT
FOR**

MFN 1.2 μ m Silicon Gate CMOS (B12)

MAXIM Integrated Products

**120 San Gabriel Dr.
Sunnyvale, CA 94086**

**This Report was prepared by
Maxim Reliability Engineering**

Summary:

The data in the tables that follow was generated as the result of an on-going Process Reliability Monitor. The specific products in this process monitor are:

MAX1772EEI+	MAX4478AUD+	MAX5621UTK+
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The calculated failure rate for devices using this process is:

FAILURE RATE: MTTF (YRS): 10852 QUANTITY: 80 FAILS: 0 FITS 10.5

The parameters used to calculate this failure rate are as follows:

Cf: 60% Ea: 0.7 Tu: 25 °C

The reliability data follows and in this section is the detailed reliability data by stress. The reliability data section includes the latest data available. This report covers data between 10/1/2009 and 9/30/2010 .

Process Information:

Process Description: MFN 1.2µm Silicon Gate CMOS (B12)

OPERATING LIFE

DESCRIPTION	DATE CODE	TEST VEHICLE	CONDITION	READPOINT	QUANTITY	FAILS	LOT NO.
HIGH TEMP OP LIFE	0914	MAX4478AUD+	135C	700 HRS	80	0	J2P0EA031B#
Total:						0	

STORAGE LIFE

DESCRIPTION	DATE CODE	TEST VEHICLE	CONDITION	READPOINT	QUANTITY	FAILS	LOT NO.
STORAGE LIFE	1014	MAX1772EEI+	150C	1000 HRS	80	0	J3ZAJA015Q1
STORAGE LIFE	1014	MAX1772EEI+	150C	1000 HRS	80	0	J3ZAJA015Q2
STORAGE LIFE	1014	MAX1772EEI+	150C	1000 HRS	80	0	J3ZAJA015Q3
Total:						0	

TEMPERATURE CYCLE

DESCRIPTION	DATE CODE	TEST VEHICLE	CONDITION	READPOINT	QUANTITY	FAILS	LOT NO.
TEMP CYCLE, 5' RAMP, 10' DWELL	1014	MAX1772EEI+	-65C TO 150C	1000 CYS	80	0	J3ZAJA015Q1
TEMP CYCLE, 5' RAMP, 10' DWELL	1014	MAX1772EEI+	-65C TO 150C	1000 CYS	80	0	J3ZAJA015Q2
TEMP CYCLE, 5' RAMP, 10' DWELL	1014	MAX1772EEI+	-65C TO 150C	1000 CYS	80	0	J3ZAJA015Q3
TEMP CYCLE, 5' RAMP, 10' DWELL	1031	MAX5621UTK+	-65C TO 150C	500 CYS	77	0	J3G0GA005Q3
TEMP CYCLE, 5' RAMP, 10' DWELL	1031	MAX5621UTK+	-65C TO 150C	500 CYS	77	0	J3G0GA005Q4
Total:						0	

FAILURE RATE: MTTF (YRS): 10852 QUANTITY: 80 FAILS: 0 FITS: 10.5