Maxim Reference Design Examples for Xilinx® FPGAs

Reference Design	Xilinx Family	Maxim Devices, Benefits, and Features
Xilinx 7-Series Family FPGA Power Module Powered by Maxim Visit www.maximintegrated.com/FPGA-Design for more information.	7-Series Family	MAX8686 25A Buck Regulator MAX8654 8A Buck Regulator MAX15041 3A Buck Regulator Benefits and Features Compact, low-cost solution Integrated MOSFETs Reliable operation '1% regulation accuracy over PVT Single/multiphase operation Remote sense Low noise transceiver supply Support all rails from 12V V _{IN} 1V, 1.5/1.35V, 1.8V, 2V, 2.5V, 3.3V, 1.2V
ZedBoard™ Powered by Maxim Visit www.maximintegrated.com/FPGA-Design for more information.	Zynq™-7000 Extensible Processing Platform	MAX15021 Dual Buck Regulator MAX15053 Buck Regulator MAX1983 LDO MAX6037A Voltage Reference for XADC MAX1510 DDR Termination Regulator Benefits and Features • Ease of use with modules and multirail regulators • Compact solution • Tight tolerance across PVT, 0.2% voltage reference • Support all rails from 5V V _{IN} • 0.75V, 1V, 1.5V, 1.8V, 2.5V, 3.3V
Virtex*-6 FPGA GTX Transceiver Power Module Powered by Maxim for Use with Xilinx ML623 Development Board Visit www.maximintegrated.com/FPGA-Design for more information.	Virtex-6 FPGA	MAX8686 25A Buck Regulator Benefits and Features Simple design to power high-speed transceivers at lowest noise Compact solution Reliable operation 'M' regulation accuracy over PVT Single/multiphase operation Remote sense Low noise transceiver supply

Maxim Reference Design Examples for Xilinx® FPGAs (continued)

Reference Design	Xilinx Family	Maxim Devices, Benefits, and Features
Spartan*-6 FPGA GTP Transceiver Power Module Powered by Maxim for Use with Xilinx SP623 Development Board Visit www.maxim-ic.com/FPGA-Design for more information.	Spartan-6 FPGA	MAX8686 25A Buck Regulator Benefits and Features Simple design to power high-speed transceivers at lowest noise Compact solution Reliable operation '1% regulation accuracy over PVT Single/multiphase operation Remote sense Low noise transceiver supply
Xilinx CoolRunnerK-II CPLD Kit Powered by Maxim Visit www.maxim-ic.com/FPGA-Design for more information.	CoolRunner-II CPLD	 MAX15007 High-Voltage LDO DS28E01-100 1-Wire Secure EEPROM Benefits and Features Industry's highest efficiency and compact design 2mm x 2mm packages, PFM mode Fully synchronous operation Solution size smaller than a penny Robust IP protection with 1-Wire* EEPROM



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