

**i2C ADDRESS A0h**

00h

EEPROM  
(256 BYTES)

FFh

**i2C ADDRESS A2h  
(DEFAULT)**

00h
<b>LOWER MEMORY</b>
PASSWORD ENTRY (PWE) (4 BYTES)
TABLE SELECT BYTE <b>7Fh</b>

AUXILIARY DEVICE  
MAIN DEVICE

**NOTE 1:** IF ASEL = 0, THEN THE MAIN DEVICE i2C SLAVE ADDRESS IS A2h.  
IF ASEL = 1, THEN THE MAIN DEVICE i2C SLAVE ADDRESS IS DETERMINED BY THE VALUE IN 8Ch TABLE 02h

**NOTE 2:** TABLE 00h DOES NOT EXIST

**NOTE 3:** ALARM ENABLE ROW CAN BE CONFIGURED TO EXIST AT TABLE 01h OR TABLE 05h USING MASK BIT IN REGISTER 89h, TABLE 02h.

80h
<b>TABLE 01h</b>
EEPROM (120 BYTES)
<b>F7h</b>
F8h
ALARM ENABLE ROW (8 BYTES)
<b>FFh</b>

80h
<b>TABLE 02h</b>
NON LOOKUP TABLE CONTROL AND CONFIGURATION REGISTERS
<b>FFh</b>

80h
<b>TABLE 04h</b>
MOD LOOK-UP TABLE (72 BYTES)
<b>C7h</b>
F8h
MOD OFFSET LUT
<b>FFh</b>

ALARM ENABLE ROW CAN BE CONFIGURED TO EXIST AT TABLE 01h OR TABLE 05h USING MASK BIT IN REGISTER 89h, TABLE 02h.

F8h
ALARM ENABLE ROW (8 BYTES)
<b>FFh</b>

80h
<b>TABLE 06h</b>
TRACKING ERROR LOOKUP TABLE (36 BYTES)
<b>A3h</b>
F8h
HBIAS LUT
<b>FFh</b>

80h
<b>TABLE 07h</b>
DAC1 LUT
<b>C7h</b>
F8h
DAC1 OFFSET LUT
<b>FFh</b>

80h
<b>TABLE 08h</b>
DAC2 LUT
<b>C7h</b>
F8h
DAC2 OFFSET LUT
<b>FFh</b>