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A37 fx

	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R
1	<b>Switching-Speed Calculation for the DS2712 Switch-Mode Battery Charger</b>																	
2																		
3		See Tab 2 for speed results																
4		See Tab 3 for reference schematic																
5																		
6																		
7																		
8	<b>Parameter</b>	<b>Value</b>		<b>Notes</b>														
9	Vin	5		Charge source voltage														
10	Vdiode	0.35		Charge source protection diode drop														
11	Qsw drop	0.133		Vds(on) or Vce(sat) for the switching transistor at the Icharge operating point														
12	Qcc drop	0.133		Charge control transistor Vds(on) or Vce(sat) at Icharge operating point (if applicable to circuit such as DS2712)														
13	Rparasitic	0.2		Other significant parasitic resistances in the Icharge path such as battery holders, connectors, fuses, etc.														
14	Vcatch	0.35		Catch diode voltage drop														
15	Rsense	0.1		Icharge sense resistor														
16	L	4.70E-05		Switch-mode inductor														
17	Battery Cells	2		Number of cells in charge circuit														
18																		
19	Vbatt min	2		Minimum battery voltage for cell stack (1.0V per cell is minimum for fast-charge)														
20	Vbatt nom	2.9		Nominal battery voltage for cell stack (1.45V per cell is normal for bulk of fast-charge)														
21	Vbatt max	3.3		Maximum battery voltage for cell stack (1.65V per cell is maximum for fast-charge)														
22	Vbatt precharge	2		Nominal battery voltage for cell stack during precharge (1.0V per cell is maximum)														
23	Vbatt topoff	3.1		Nominal battery voltage for cell stack during topoff (1.55V per cell is a nominal value for bulk of topoff)														
24																		
25	Viref	0.125		Fast-charge voltage reference for sense resistor; see device data sheet; DS2715 = 121mV is default														
26	Vhys-comp	0.024		Hysteresis for fast-charge voltage reference; see device data sheet; DS2715 = 28mV is default														
27	Vhys-comp/2	0.012		Half of the fast-charge hysteresis value														
28	Vsense-fc	0.113		DC voltage regulated across sense resistor in fast-charge is the voltage reference for the DS2712														
29																		
30	tpdly	2.00E-07		Internal Vsense comparator time delay; 200ns is a typical value														
31	tsw on	2.00E-07		Switching transistor turn-on time delay from control signal request (if applicable; more likely necessary in bipolar circuits; often much less than tsw_off)														
32	tsw off	2.00E-07		Switching transistor turn-off time delay from control signal request (if applicable; more likely necessary in bipolar circuits where 2us or more is possible)														
33																		
34																		