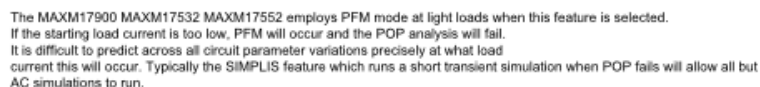


Initial Design

1.0

Design Requirements

Parameter	Value
Minimum Input Voltage	10V
Nominal Input Voltage	24V
Maximum Input Voltage	42V
Input Steady-State Ripple	0.48V
Input Undervoltage Lockout Level	4.2V
Output Voltage	5V
Output Current	0.1A
Output Voltage Load Step Overshoot/Undershoot	0.15V
BOM Priority	Cost
Switching Frequency	600kHz
MODE of Operation	PWM
Soft Start Time	5.1ms
Ambient Temperature	25°C



Ref	Qty	Part Number	Manufacturer	Description
U1	1	MAXM17532	User-Defined	IC
C3	1	ECHU1H333GX5	Panasonic	Cap Film 0.033uF 50V PPS 2% (3.2 X 2.5 X 2.1mm) Stacked 125°C T/R
CIN	1	GRM31CR71H225KA88	Murata	Cap Ceramic 2.2uF 50V X7R 10% SMD 1206 125C
COUT	1	C2012X7R1A106K125AC	TDK	Cap Ceramic 10uF 10V X7R 10% SMD 0805 125C Plastic T/R
R1	1	ERJ2RKF2613X	Panasonic	Res Thick Film 0402 261K Ohm 1% 0.1W(1/10W) ±100ppm/°C Pad SMD Automotive T/R
R2	1	ERJ3EKF4992V	Panasonic	Res Thick Film 0603 49.9K Ohm 1% 0.1W(1/10W) ±100ppm/°C Pad SMD Automotive T/R
				Res Thick Film 0603 69.8K Ohm 1%

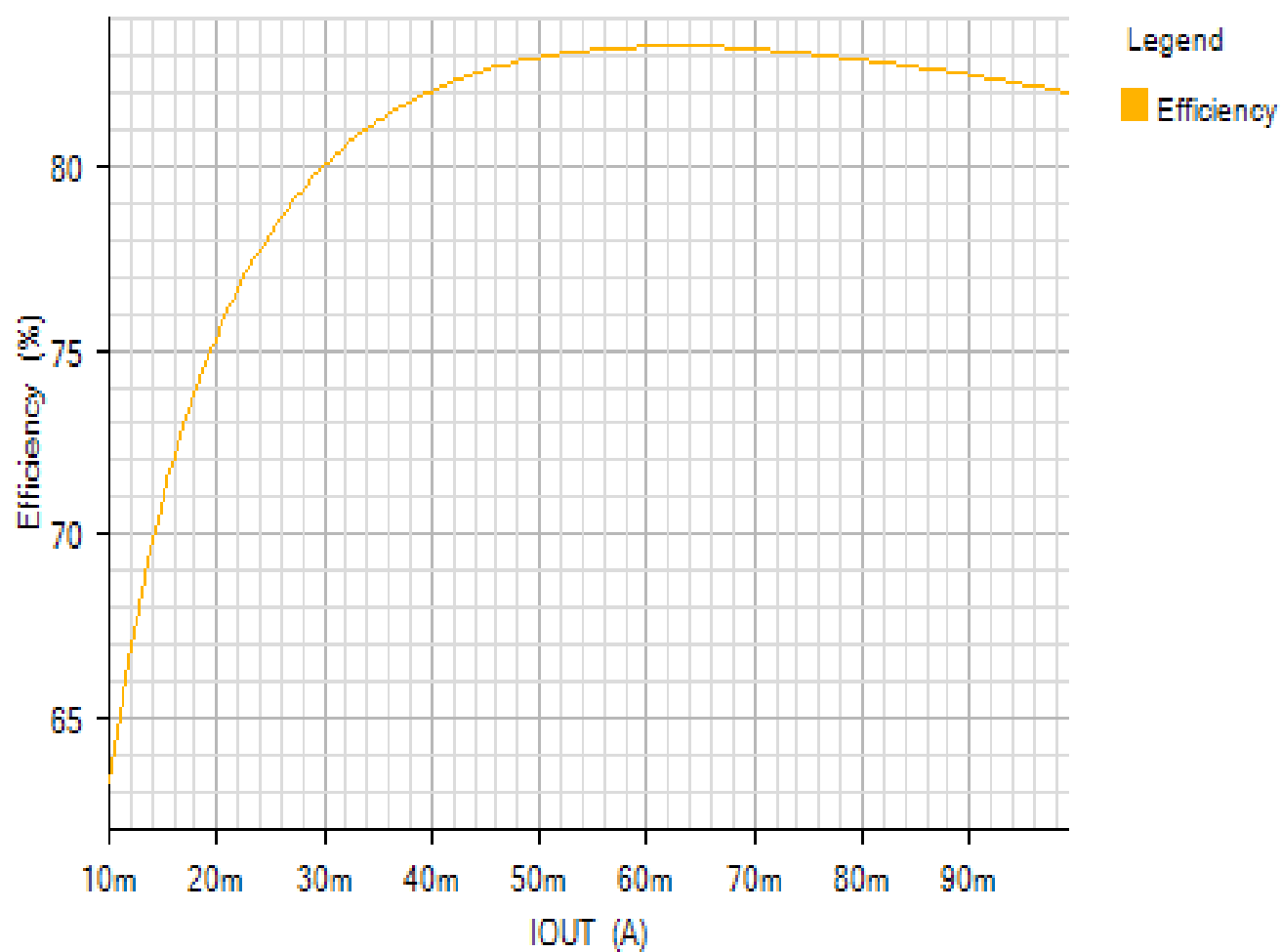
R3	1	ERJ3EKF6982V	Panasonic	0.1W(1/10W) $\pm 100\text{ppm}/^\circ\text{C}$ Pad SMD Automotive T/R
R4	1	ERJ2GEJ103X	Panasonic	Res Thick Film 0402 10K Ohm 5% 0.1W(1/10W) $\pm 200\text{ppm}/^\circ\text{C}$ Pad SMD Automotive T/R

Simulation Results

Efficiency - Mon Nov 26 2018 15:28:33

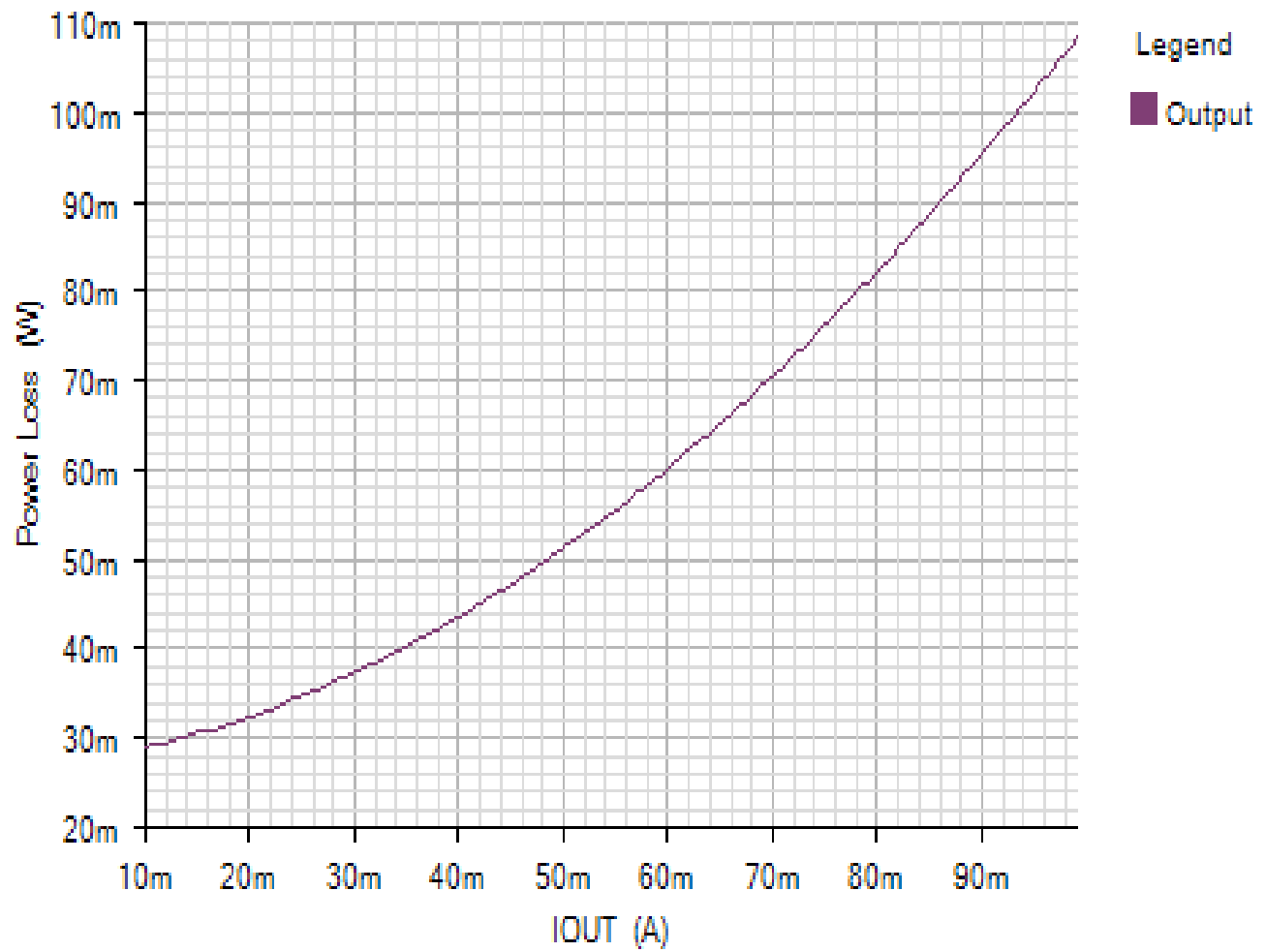
EFFICIENCY_PLOT

Default



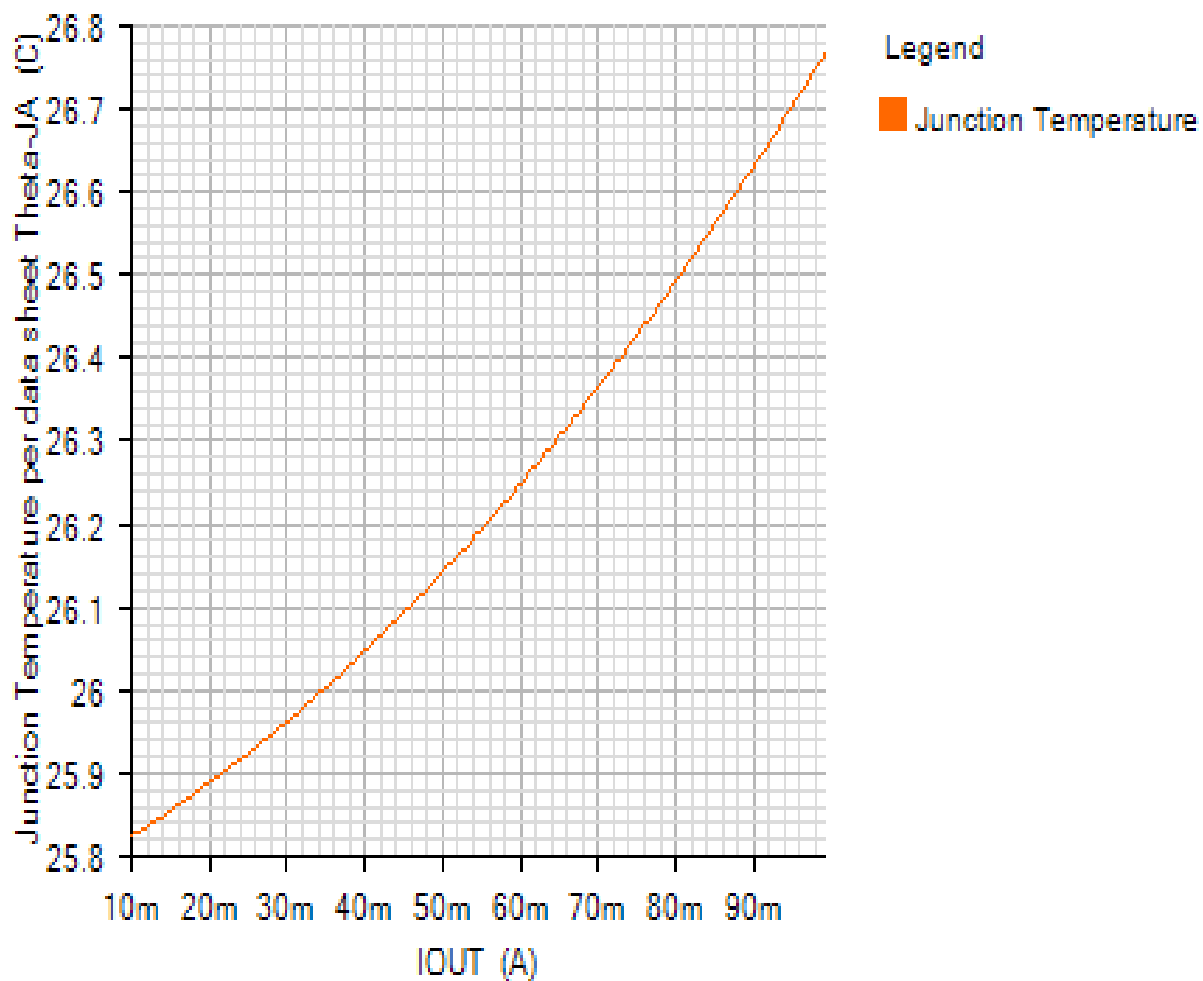
POWER_LOSS_PLOT

Default



JUNCTION_TEMPERATURE_PLOT

Default



Losses



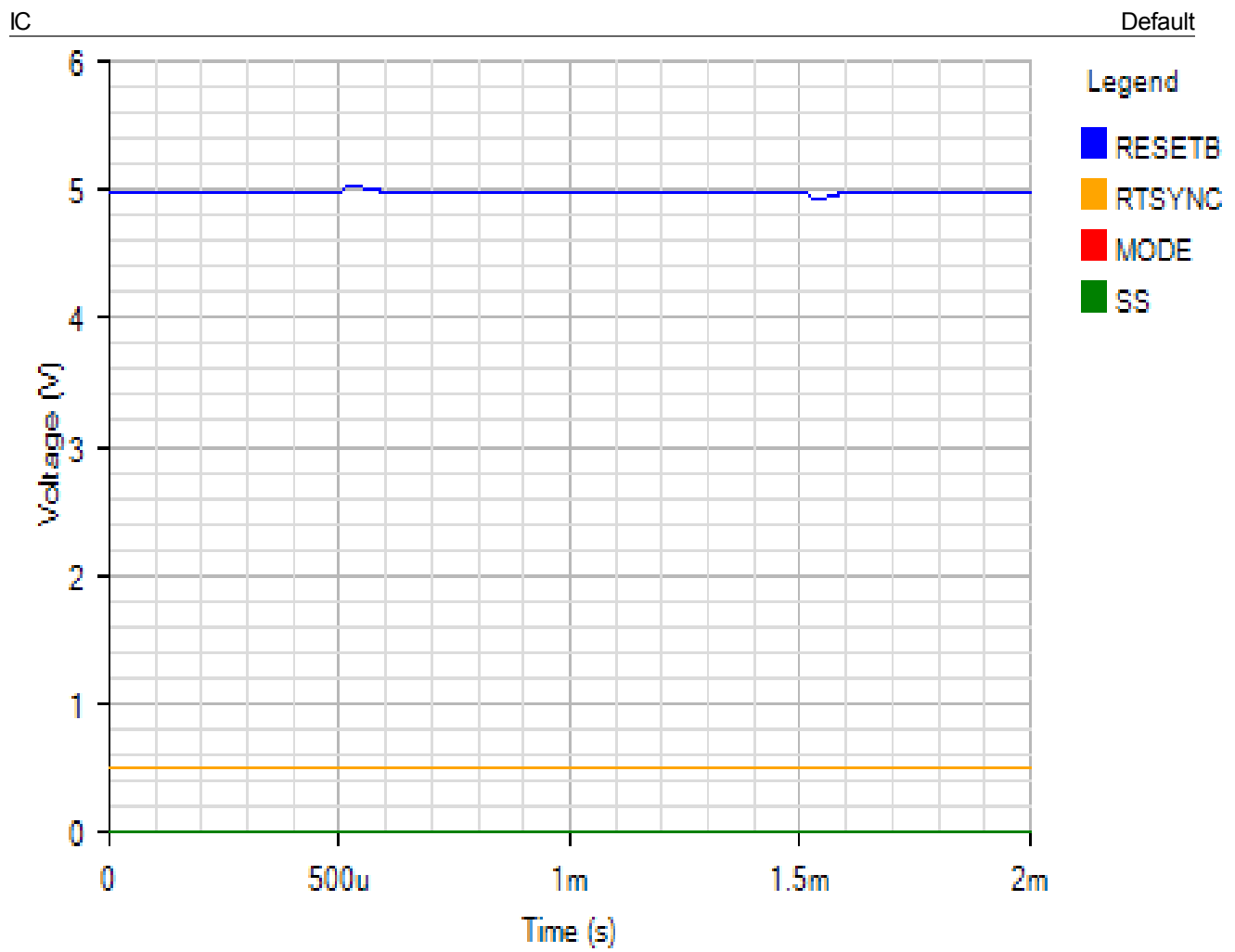
Component

Loss (W)

% of total

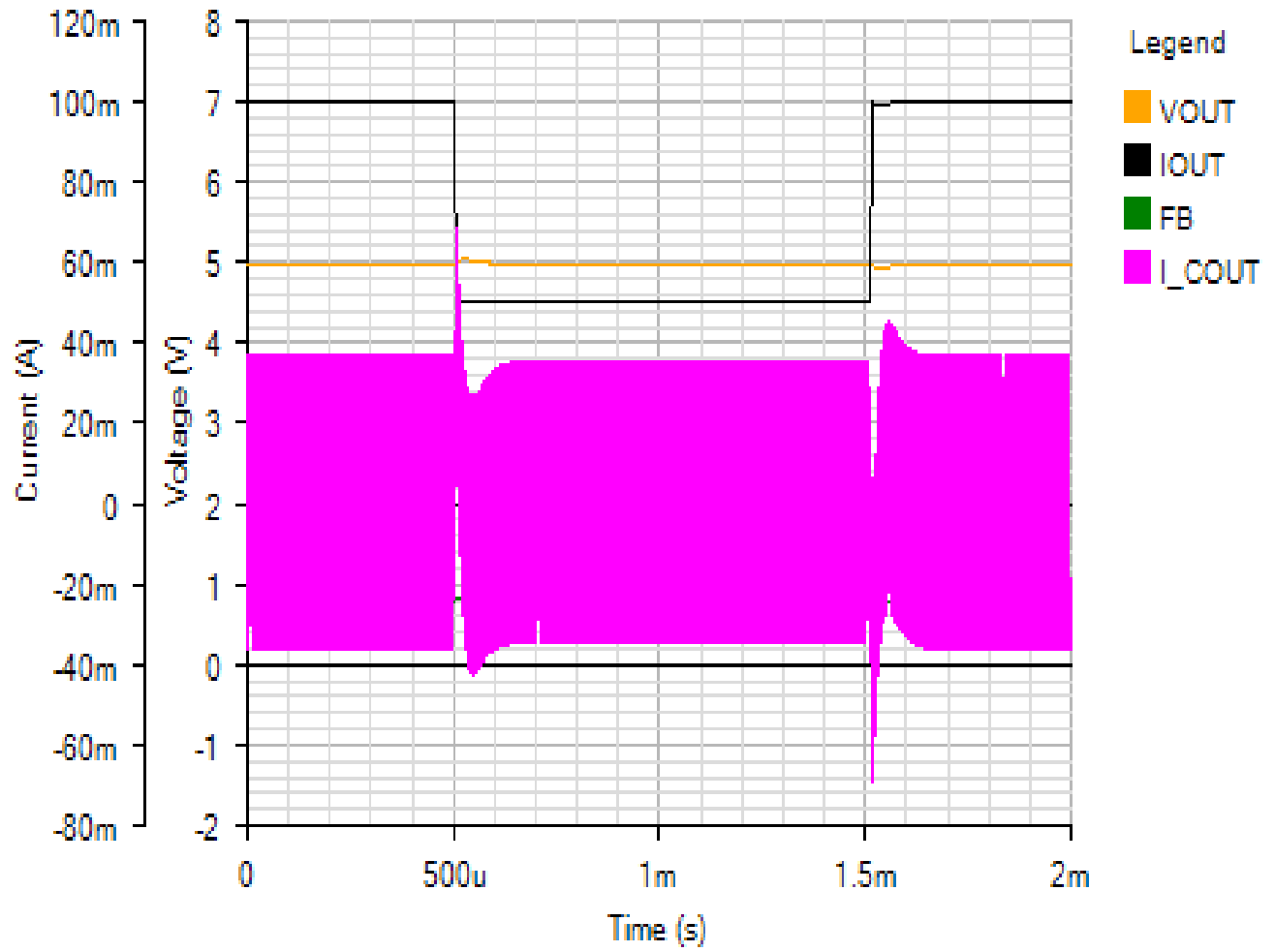
Component	Loss (W)	% of total
Inductor losses	0.05	45.5
Losses in IC	0.06	54.5
Total	0.11	100

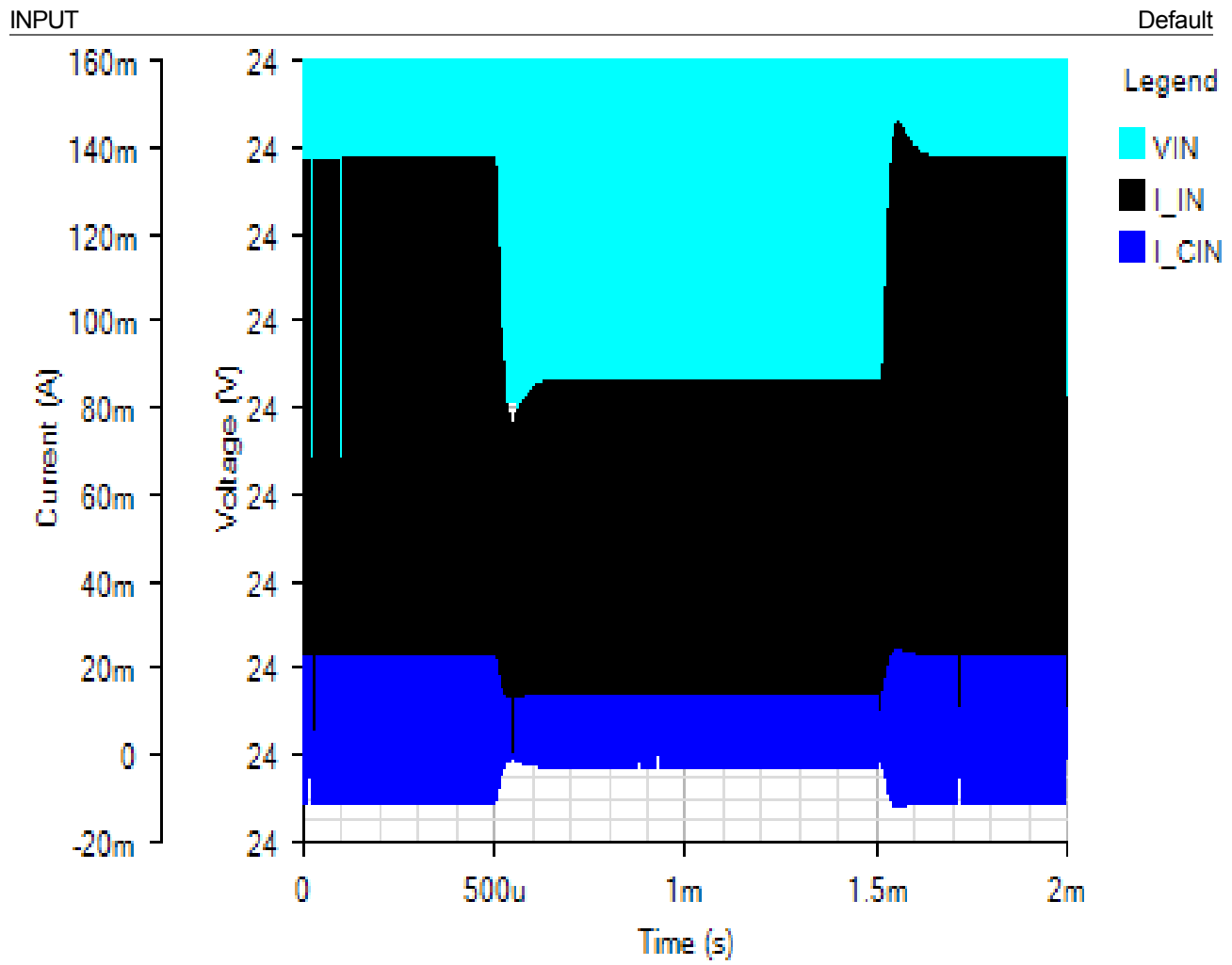
Load Step - Mon Nov 26 2018 15:28:33



OUTPUT

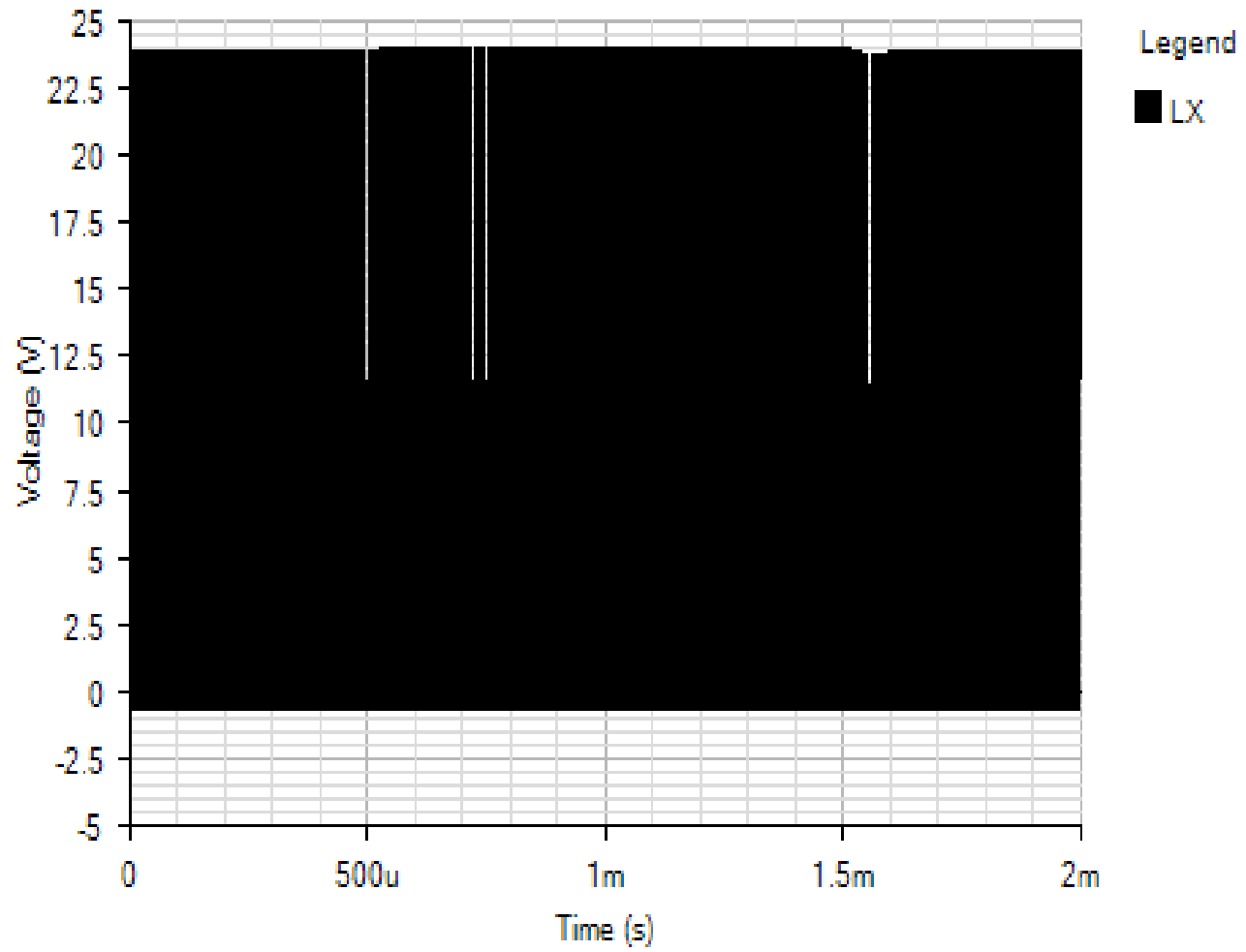
Default





SWITCHING

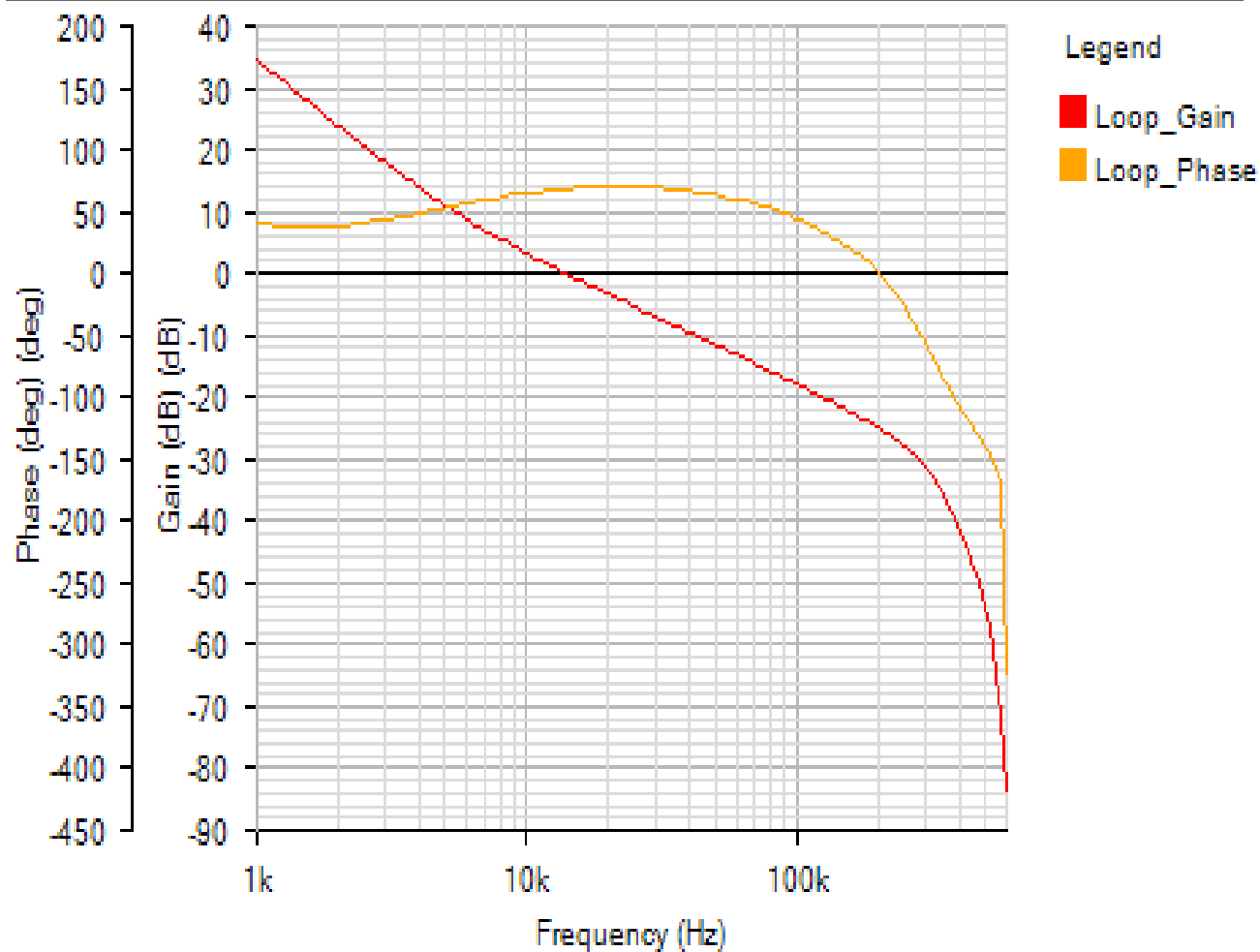
Default



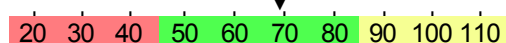
AC Loop - Mon Nov 26 2018 15:28:33

BODE

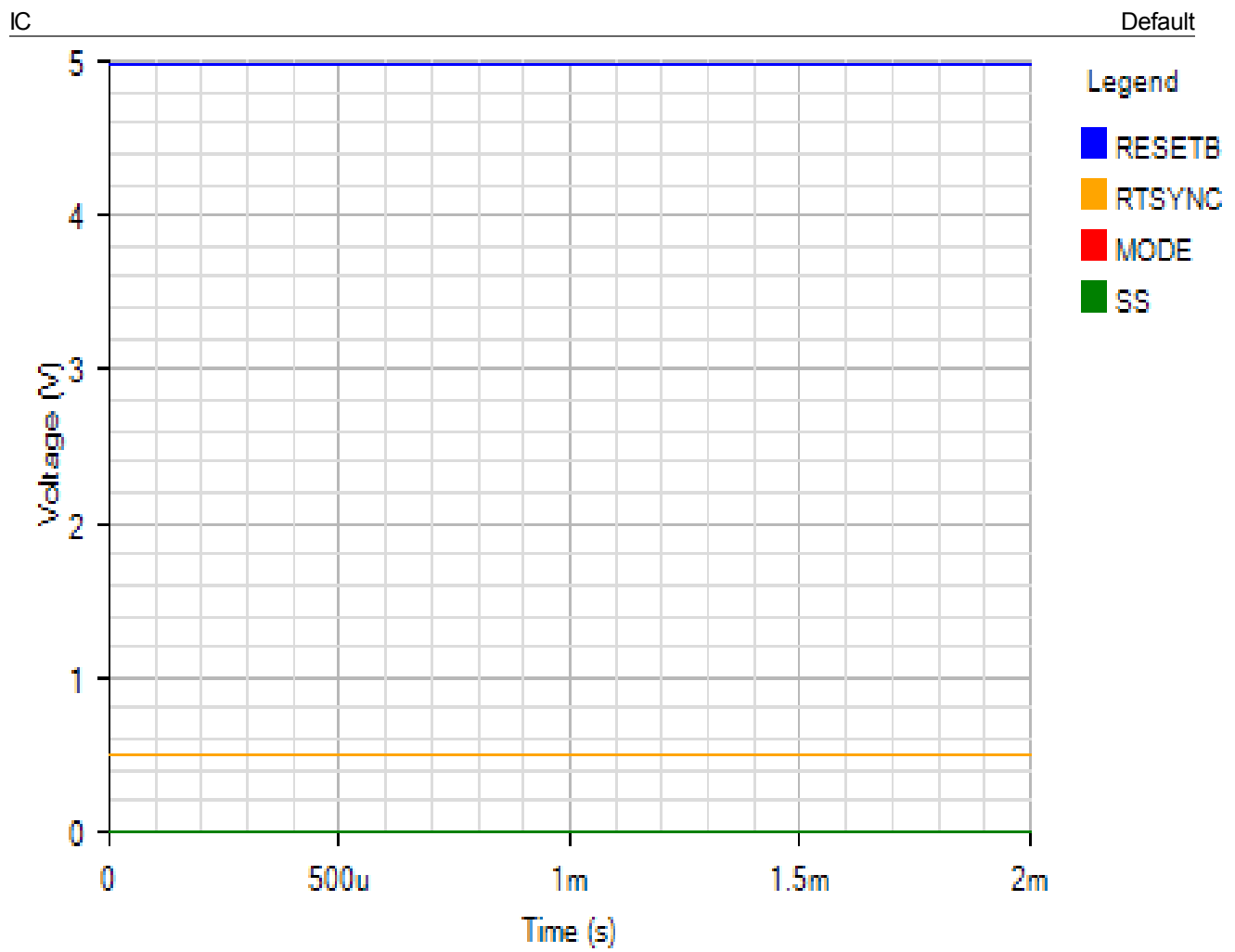
Default



Phase Margin: 68.85° at a crossover frequency of 14.1kHz

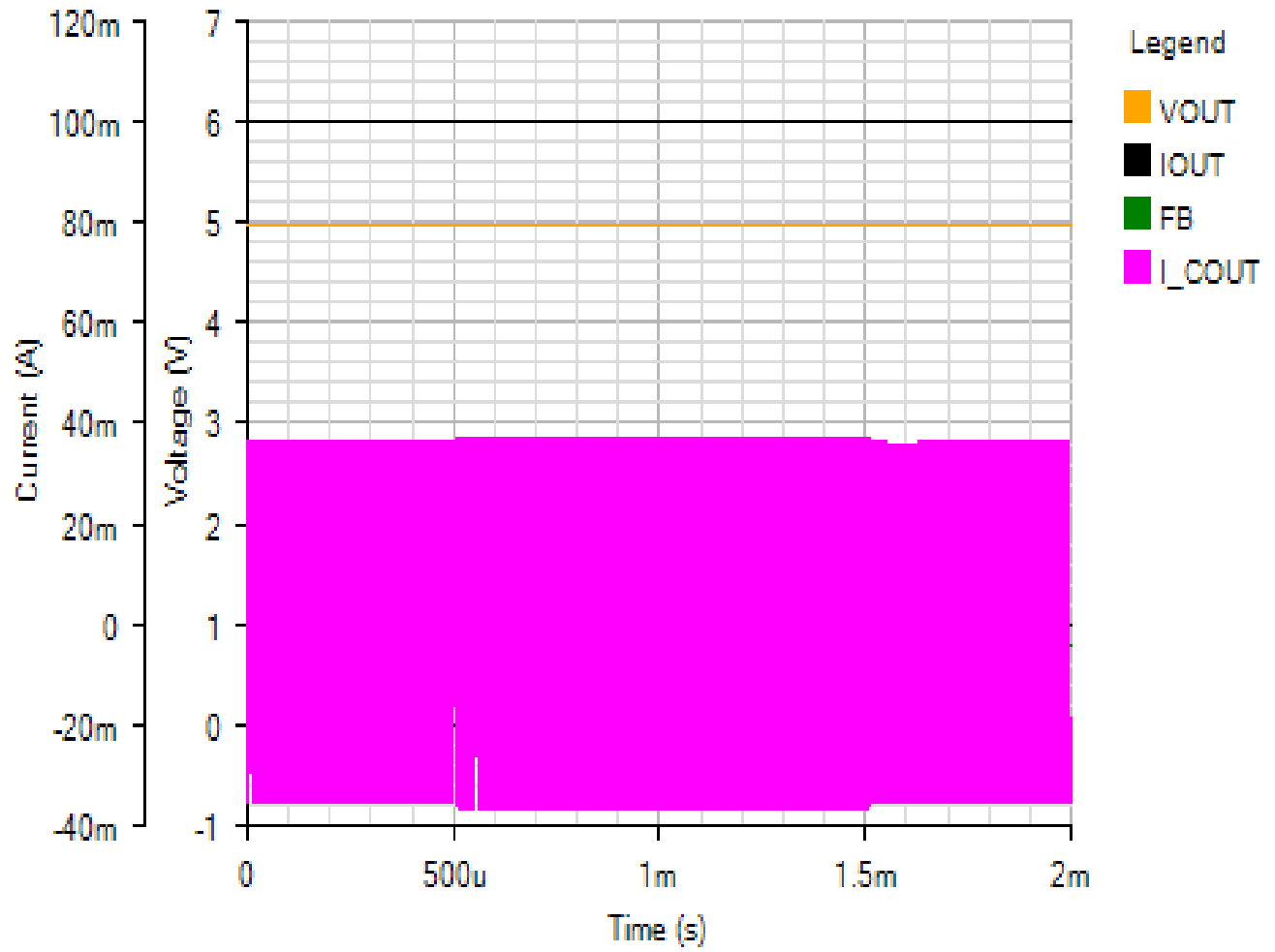


Line Transient - Mon Nov 26 2018 15:28:33



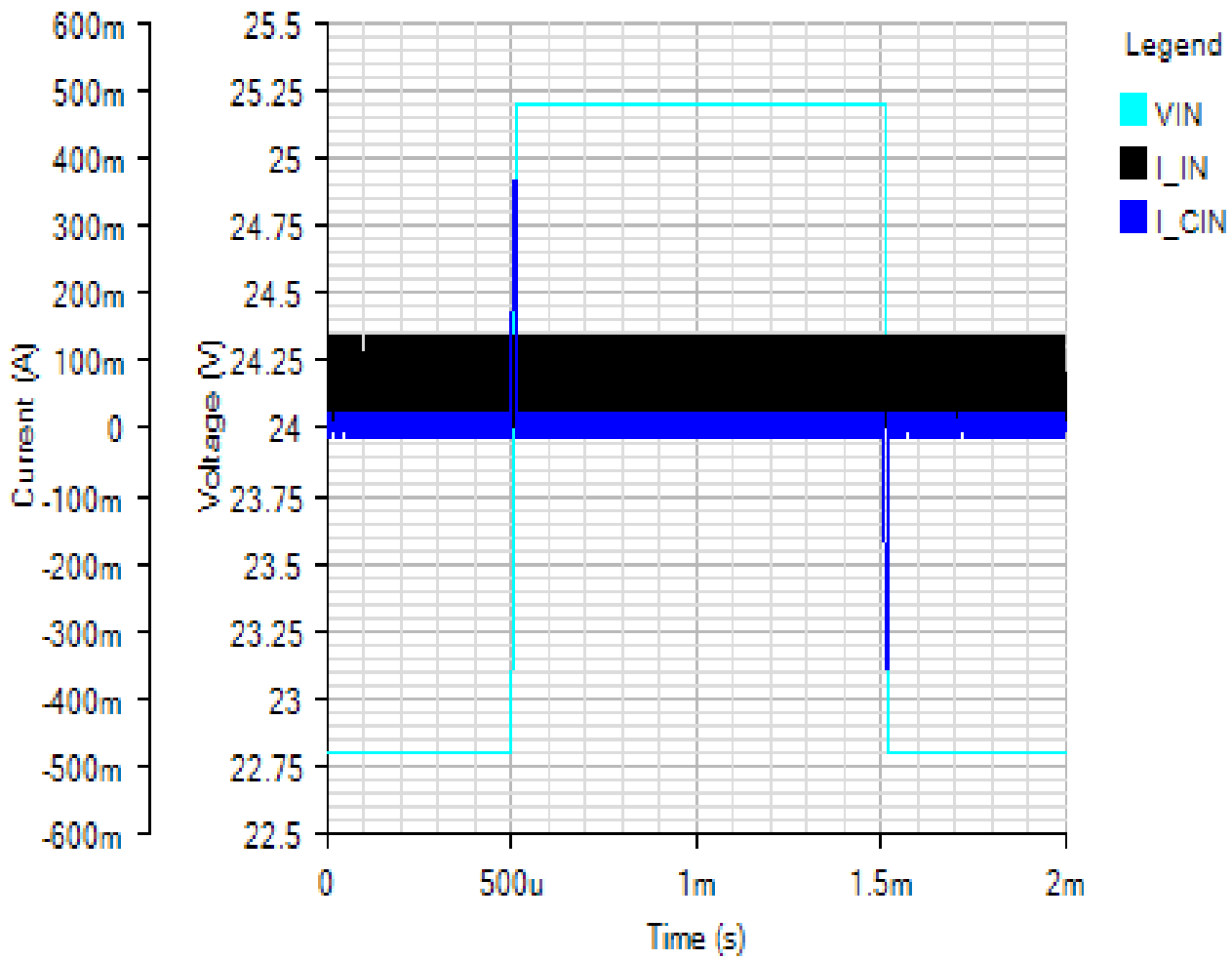
OUTPUT

Default



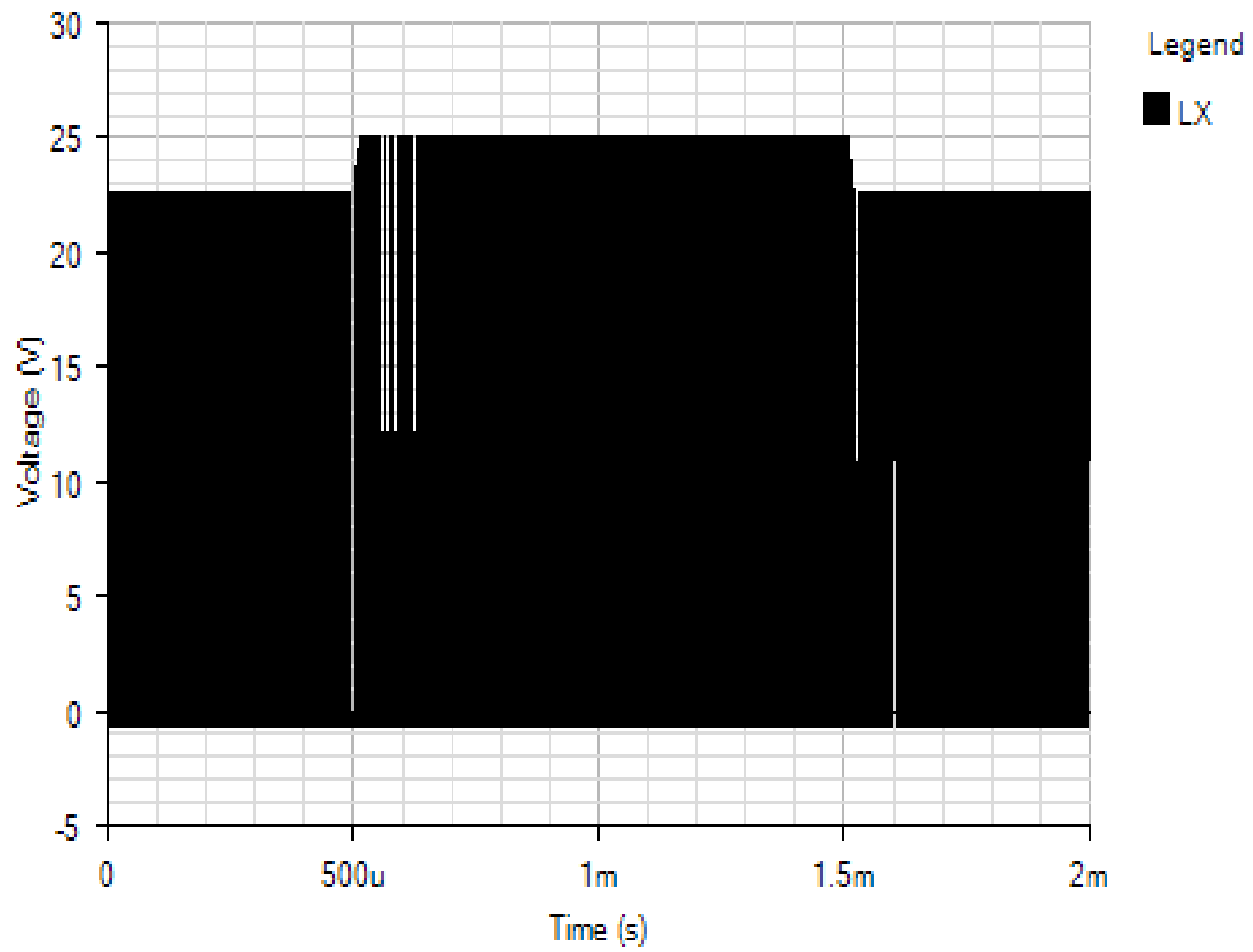
INPUT

Default



SWITCHING

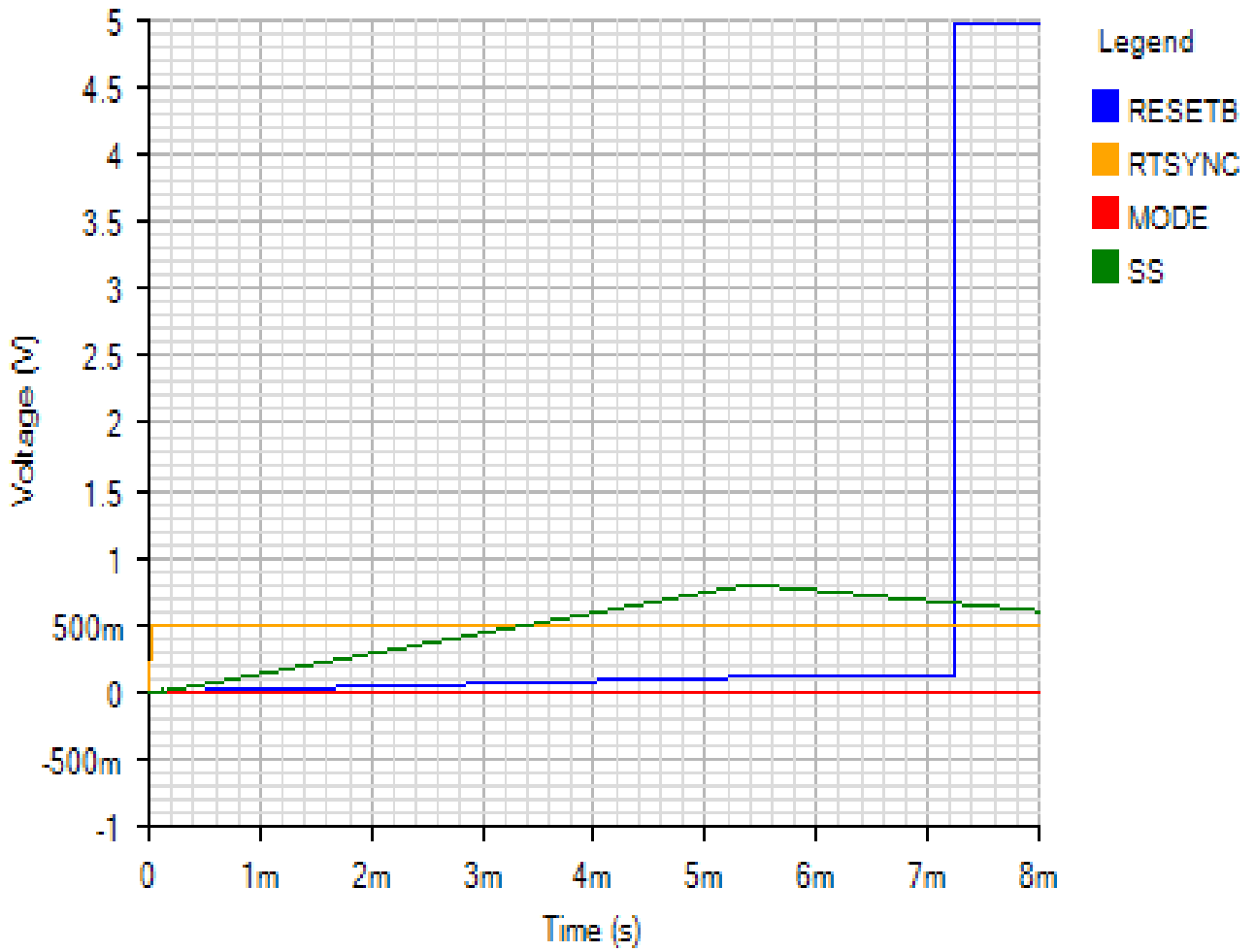
Default



Start Up - Mon Nov 26 2018 15:28:33

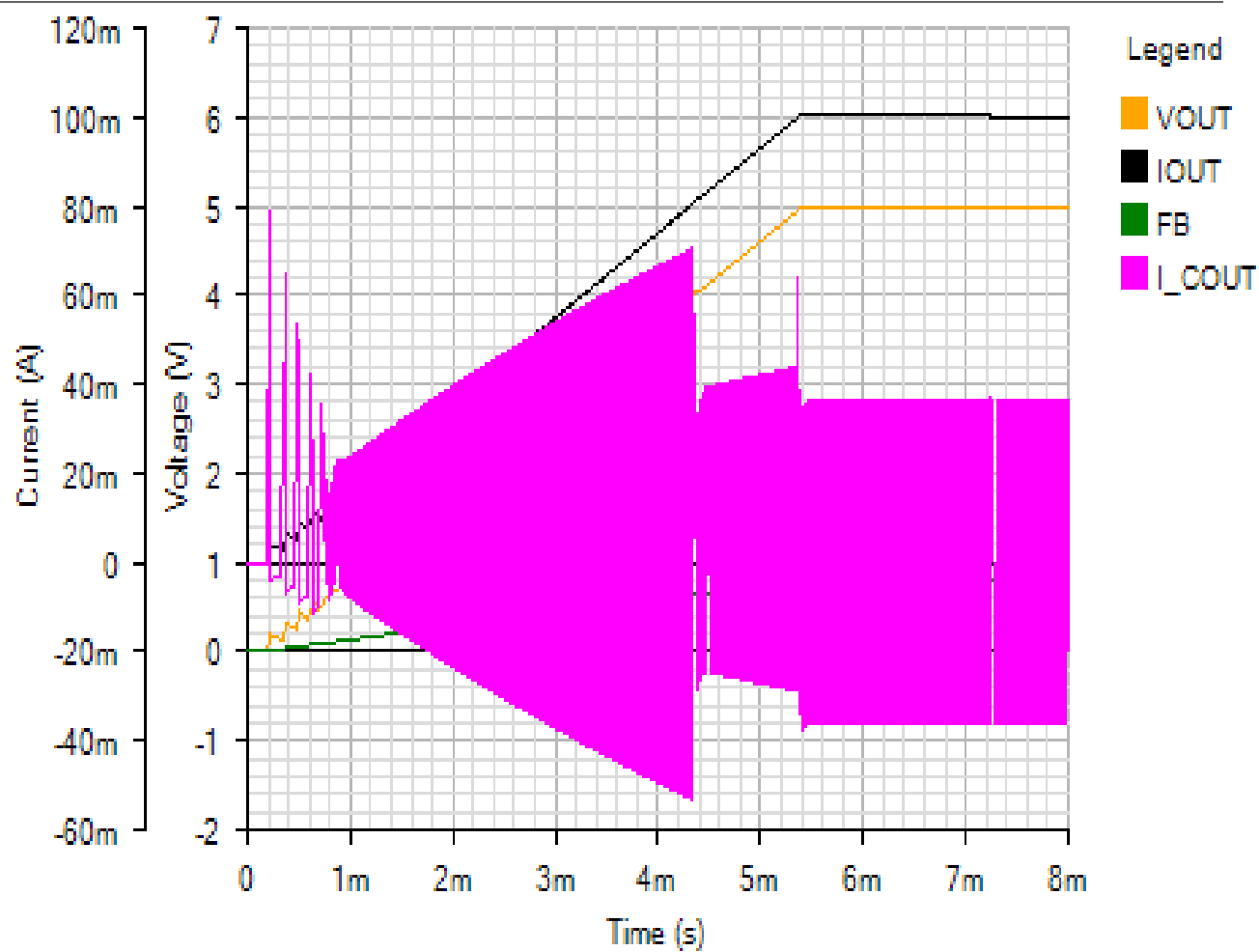
IC

Default



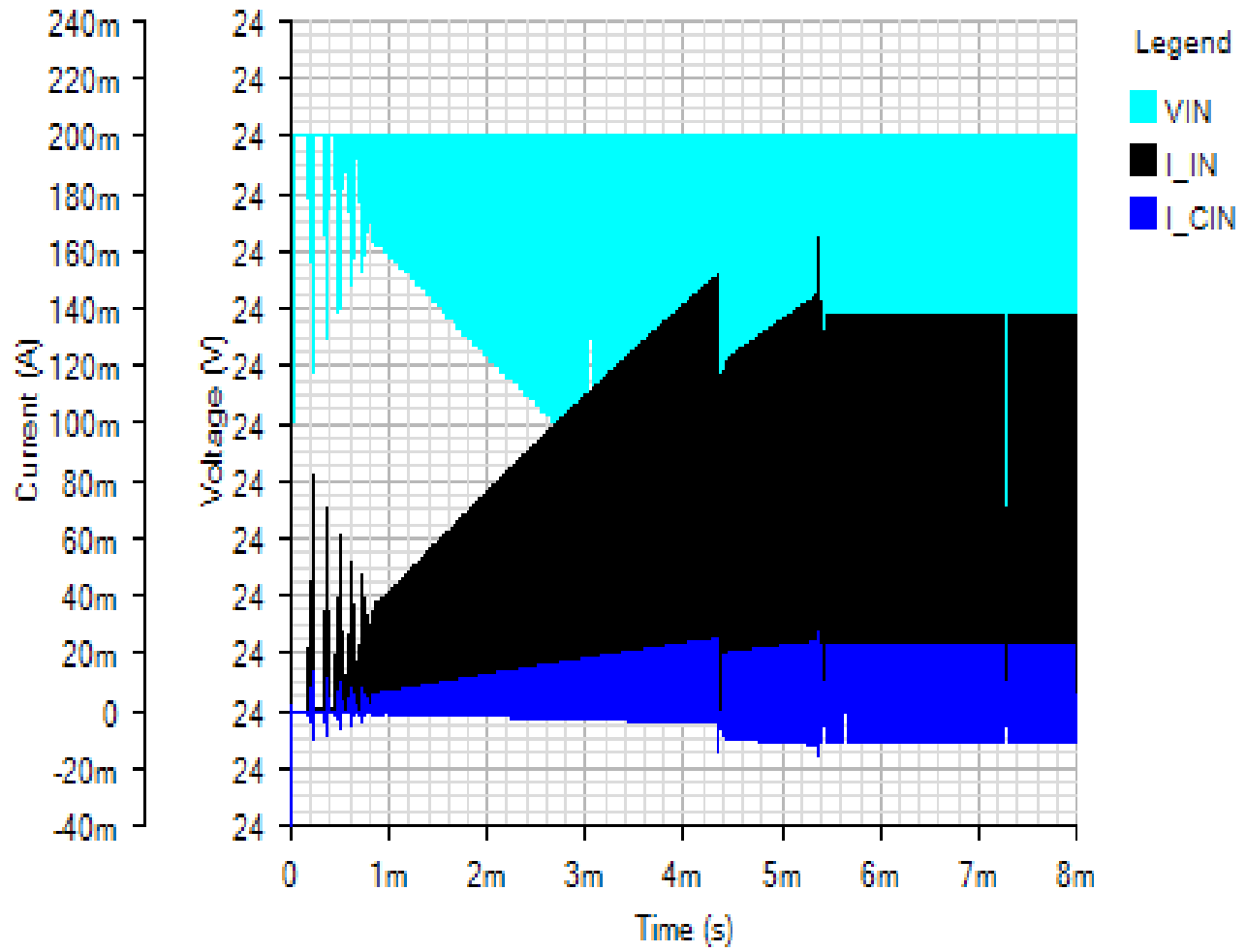
OUTPUT

Default



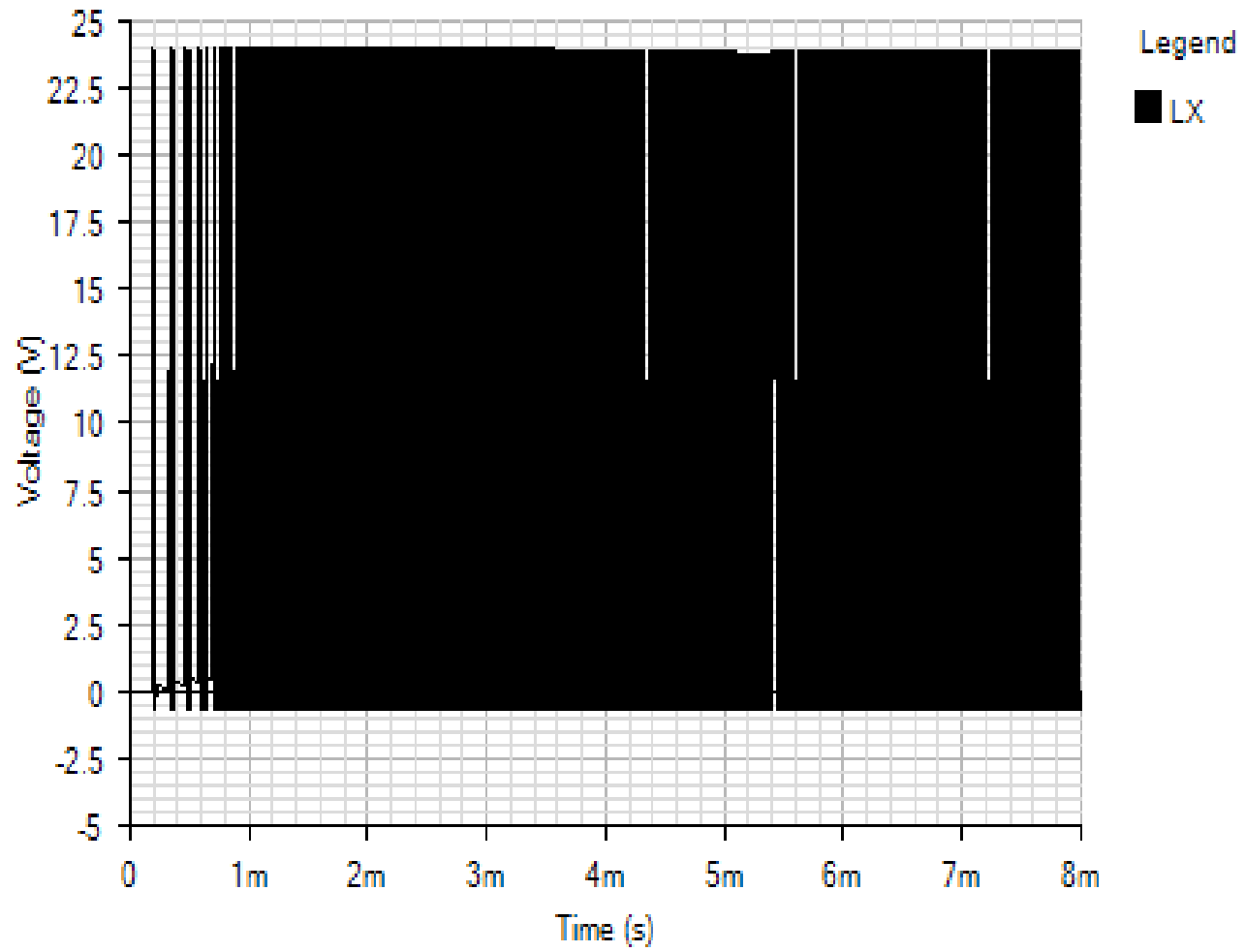
INPUT

Default

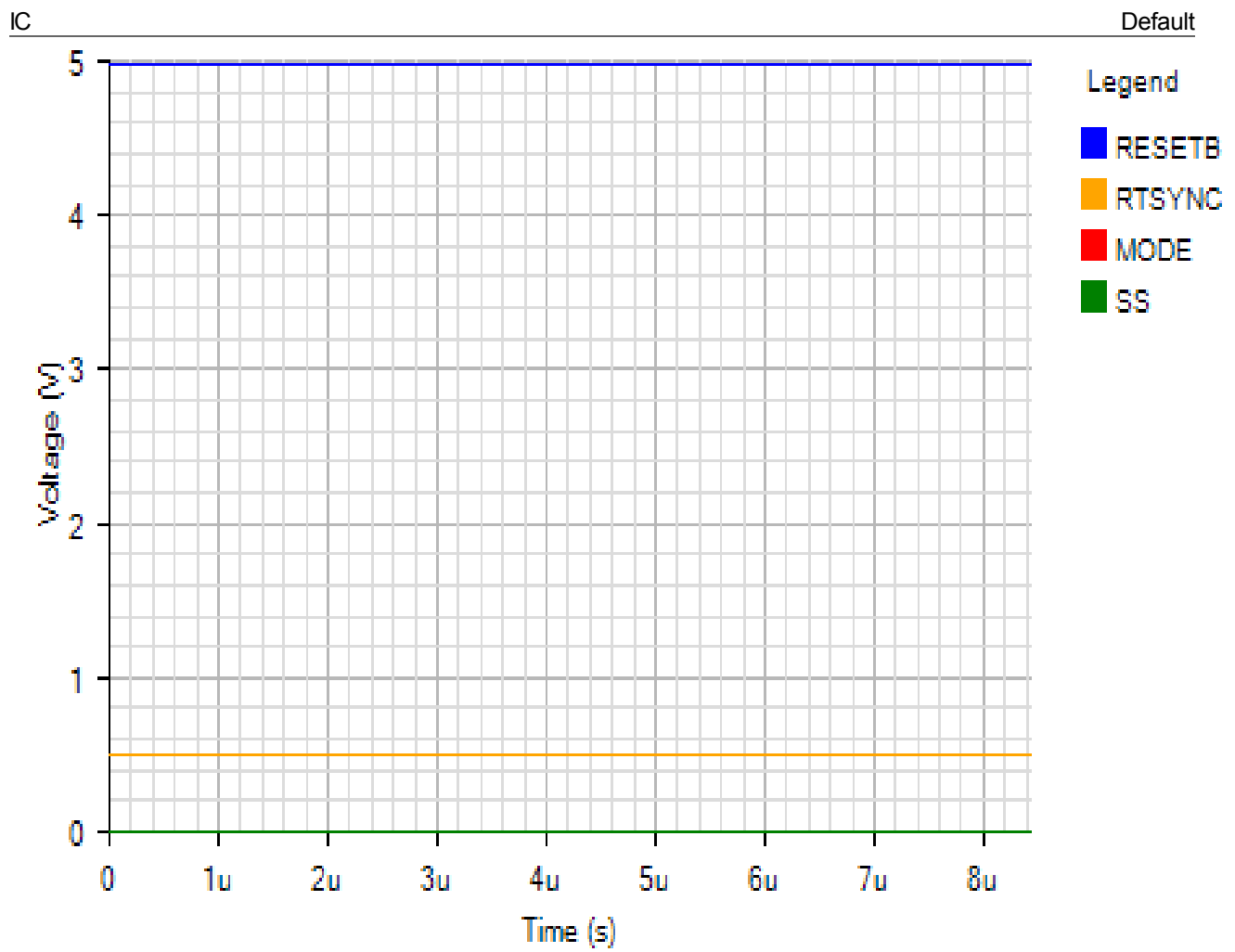


SWITCHING

Default

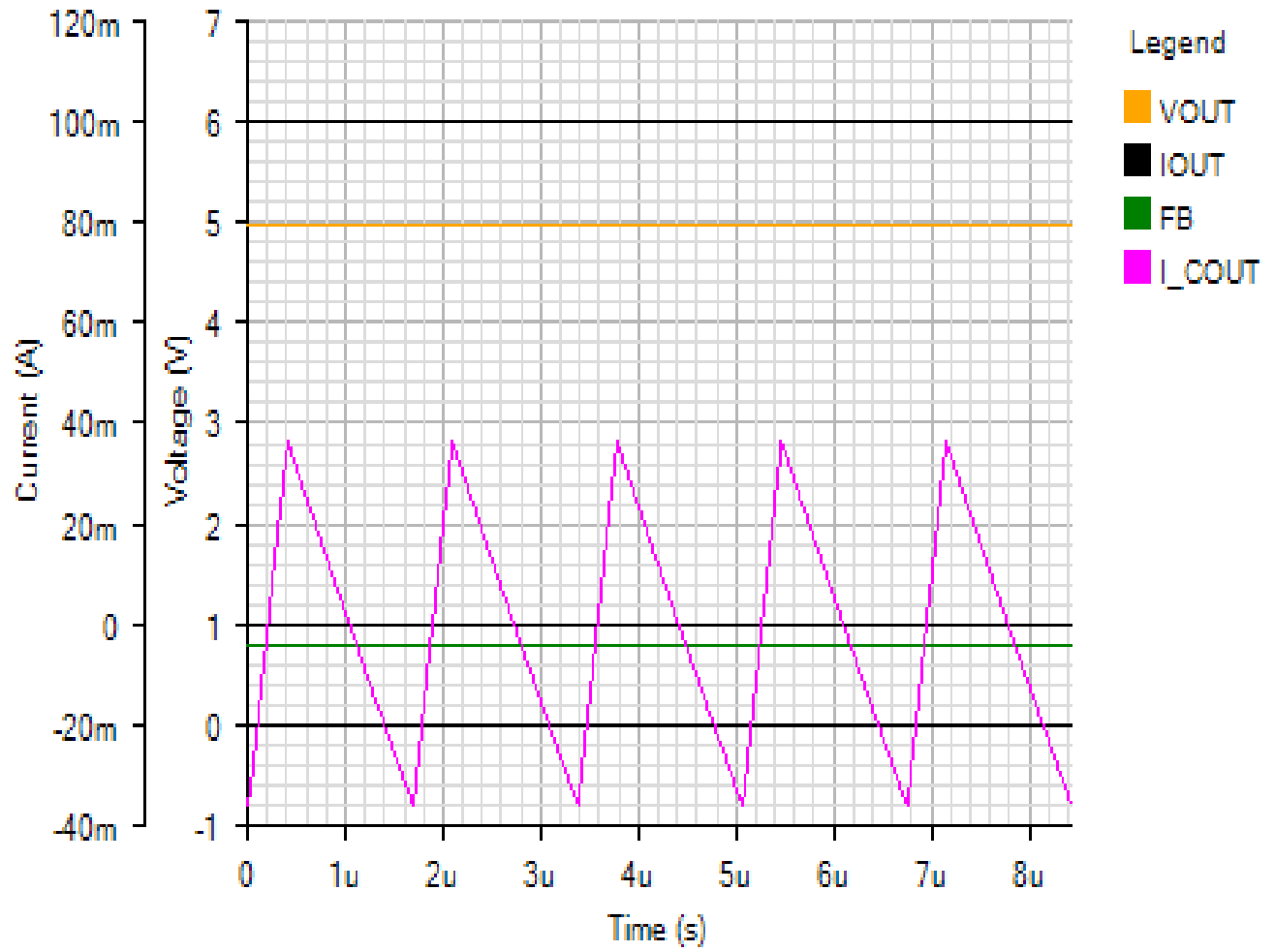


Steady State - Mon Nov 26 2018 15:28:33



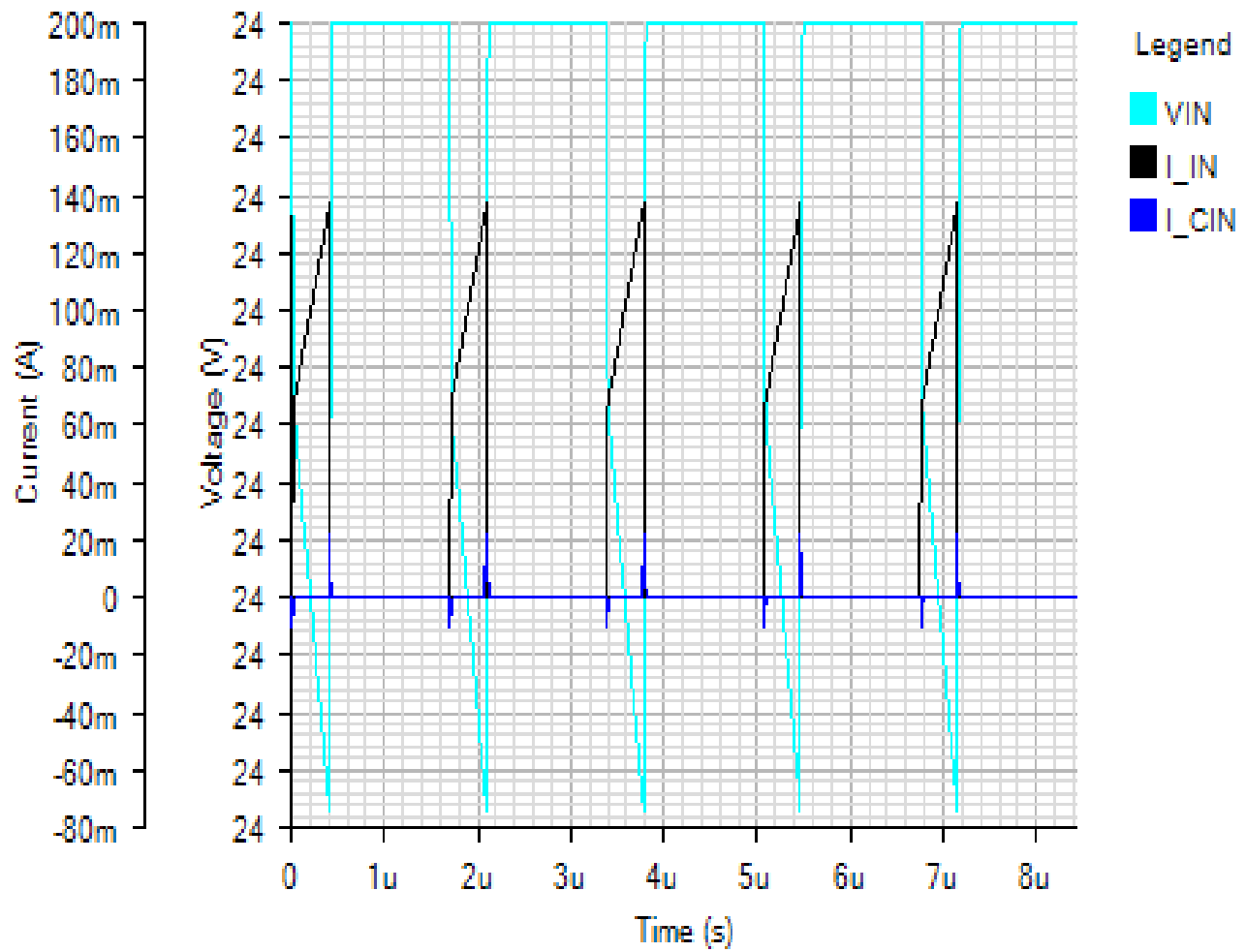
OUTPUT

Default



INPUT

Default



SWITCHING

Default

