

RELIABILITY REPORT
FOR
MAX6195BESA+
(MAX6190-MAX6195/MAX6198)
PLASTIC ENCAPSULATED DEVICES

March 5, 2009

MAXIM INTEGRATED PRODUCTS

120 SAN GABRIEL DR.
SUNNYVALE, CA 94086

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| Approved by |
| Ken Wendel |
| Quality Assurance |
| Director, Reliability Engineering |

Conclusion

The MAX6195BESA+ successfully meets the quality and reliability standards required of all Maxim products. In addition, Maxim's continuous reliability monitoring program ensures that all outgoing product will continue to meet Maxim's quality and reliability standards.

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I. Device Description

A. General

The MAX6190-MAX6195/MAX6198 precision, micro-power, low-dropout voltage references offer high initial accuracy and very low temperature coefficient through a proprietary curvature-correction circuit and laser-trimmed precision thin-film resistors. These series-mode bandgap references draw a maximum of only 35 μ A quiescent supply current, making them ideal for battery-powered instruments. They offer a supply current that is virtually immune to input voltage variations. Load-regulation specifications are guaranteed for source and sink currents up to 500 μ A. These devices are internally compensated, making them ideal for applications that require fast settling, and are stable with capacitive loads up to 2.2nF.

II. Manufacturing Information

| | |
|----------------------------------|---|
| A. Description/Function: | Precision, Micropower, Low-Dropout Voltage References |
| B. Process: | B12 |
| C. Number of Device Transistors: | |
| D. Fabrication Location: | Oregon |
| E. Assembly Location: | ATP Philippines, UTL Thailand, Carsem Malaysia, Unisem Malaysia |
| F. Date of Initial Production: | November 06, 1998 |

III. Packaging Information

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|--|--------------------------|
| A. Package Type: | 8-pin SOIC (N) |
| B. Lead Frame: | Copper |
| C. Lead Finish: | 100% matte Tin D. |
| Die Attach: | 84-1Imisr4 Epoxy |
| E. Bondwire: | Gold (1.3 mil dia.) |
| F. Mold Material: | Epoxy with silica filler |
| G. Assembly Diagram: | #05-0901-0152 |
| H. Flammability Rating: | Class UL94-V0 |
| I. Classification of Moisture Sensitivity per JEDEC standard J-STD-020-C | Level 1 |
| J. Single Layer Theta Ja: | 170°C/W |
| K. Single Layer Theta Jc: | 40°C/W |
| L. Multi Layer Theta Ja: | 136°C/W |
| M. Multi Layer Theta Jc: | 38°C/W |

IV. Die Information

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|----------------------------|---|
| A. Dimensions: | 44 X 31 mils |
| B. Passivation: | Si ₃ N ₄ /SiO ₂ (Silicon nitride/ Silicon dioxide) |
| C. Interconnect: | Aluminum/Si (Si = 1%) |
| D. Backside Metallization: | None |
| E. Minimum Metal Width: | 1.2 microns (as drawn) |
| F. Minimum Metal Spacing: | 1.2 microns (as drawn) |
| G. Bondpad Dimensions: | 5 mil. Sq. |
| H. Isolation Dielectric: | SiO ₂ |
| I. Die Separation Method: | Wafer Saw |

V. Quality Assurance Information

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|-----------------------------------|---|
| A. Quality Assurance Contacts: | Ken Wendel (Director, Reliability Engineering) Bryan Preeshl (Managing Director of QA) |
| B. Outgoing Inspection Level: | 0.1% for all electrical parameters guaranteed by the Datasheet. 0.1% For all Visual Defects. |
| C. Observed Outgoing Defect Rate: | < 50 ppm |
| D. Sampling Plan: | Mil-Std-105D |

VI. Reliability Evaluation

A. Accelerated Life Test

The results of the 135°C biased (static) life test are shown in Table 1. Using these results, the Failure Rate (λ) is calculated as follows:

$$\lambda = \frac{1}{\text{MTTF}} = \frac{1.83}{192 \times 4340 \times 80 \times 2} \quad (\text{Chi square value for MTTF upper limit})$$

(where 4340 = Temperature Acceleration factor assuming an activation energy of 0.8eV)

$$\lambda = 13.4 \times 10^{-9}$$

$$\lambda = 13.4 \text{ F.I.T. (60\% confidence level @ 25°C)}$$

The following failure rate represents data collected from Maxim's reliability monitor program. Maxim performs quarterly 1000 hour life test monitors on its processes. This data is published in the Product Reliability Report found at <http://www.maxim-ic.com/>. Current monitor data for the B8 Process results in a FIT Rate of 2.71 @ 25C and 17.30 @ 55C (0.8 eV, 60% UCL)

B. Moisture Resistance Tests

The industry standard 85°C/85%RH or HAST testing is monitored per device process once a quarter.

C. E.S.D. and Latch-Up Testing

The RF23-4 die type has been found to have all pins able to withstand a HBM transient pulse of +/-1500 V per Mil-Std 883 Method 3015.7. Latch-Up testing has shown that this device withstands a current of +/-250 mA.

Table 1
Reliability Evaluation Test Results

MAX6195BESA+

| TEST ITEM | TEST CONDITION | FAILURE IDENTIFICATION | SAMPLE SIZE | NUMBER OF FAILURES |
|---|--|----------------------------------|-------------|--------------------|
| Static Life Test (Note 1) | Ta = 135°C Biased Time = 192 hrs. | DC Parameters & functionality | 80 | 0 |
| Moisture Testing (Note 2) 85/85 | Ta = 85°C RH = 85% Biased Time = 1000hrs. | DC Parameters & functionality | 77 | 0 |
| Mechanical Stress (Note 2) Temperature Cycle | -65°C/150°C 1000 Cycles Method 1010 | DC Parameters & functionality | 77 | 0 |

Note 1: Life Test Data may represent plastic DIP qualification lots.

Note 2: Generic Package/Process data